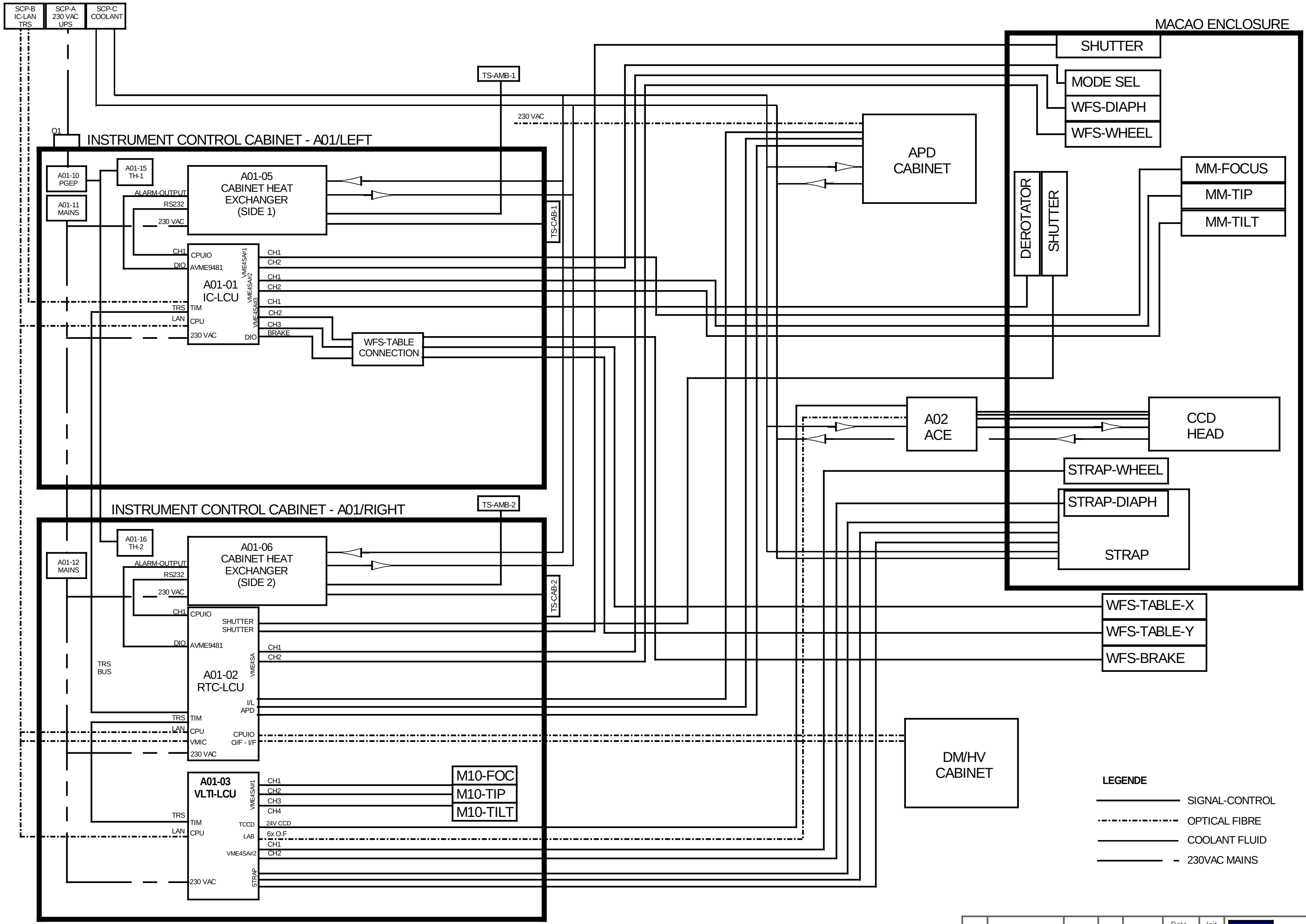
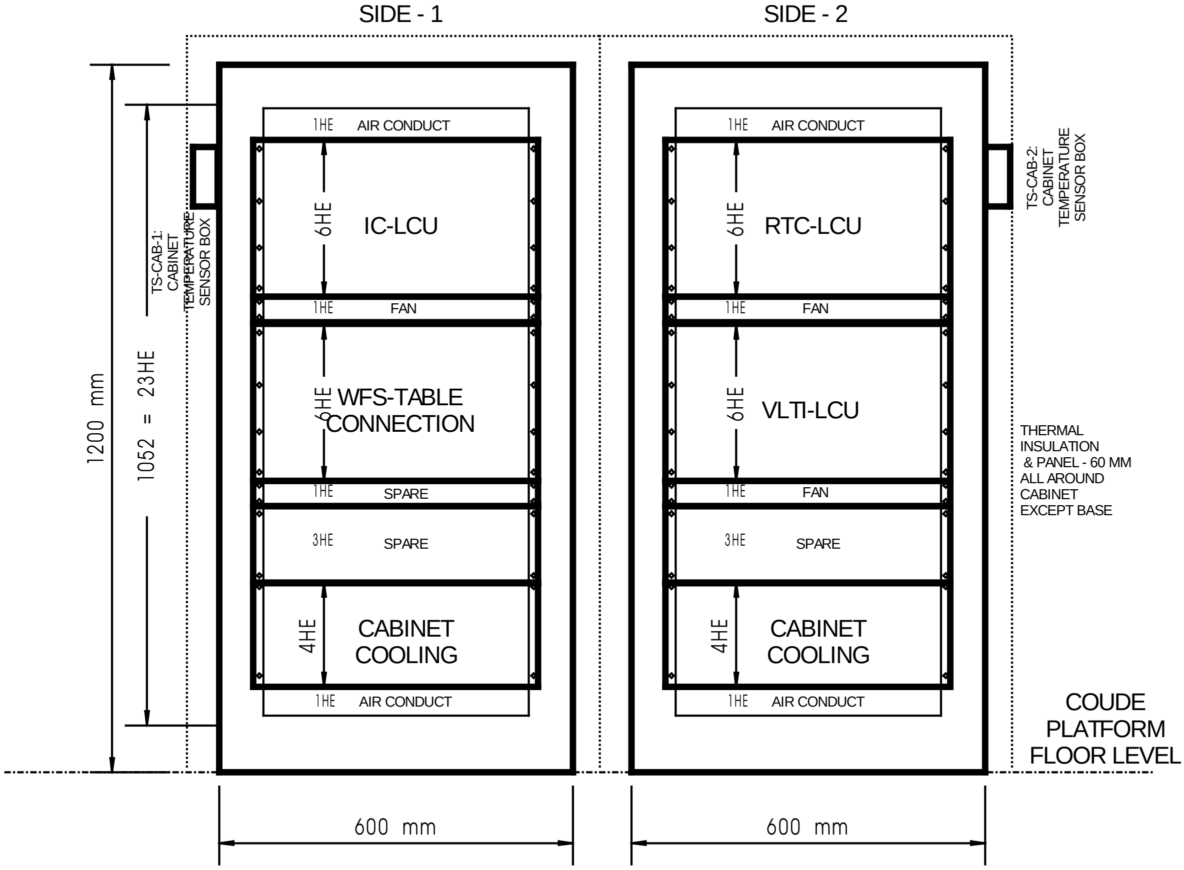
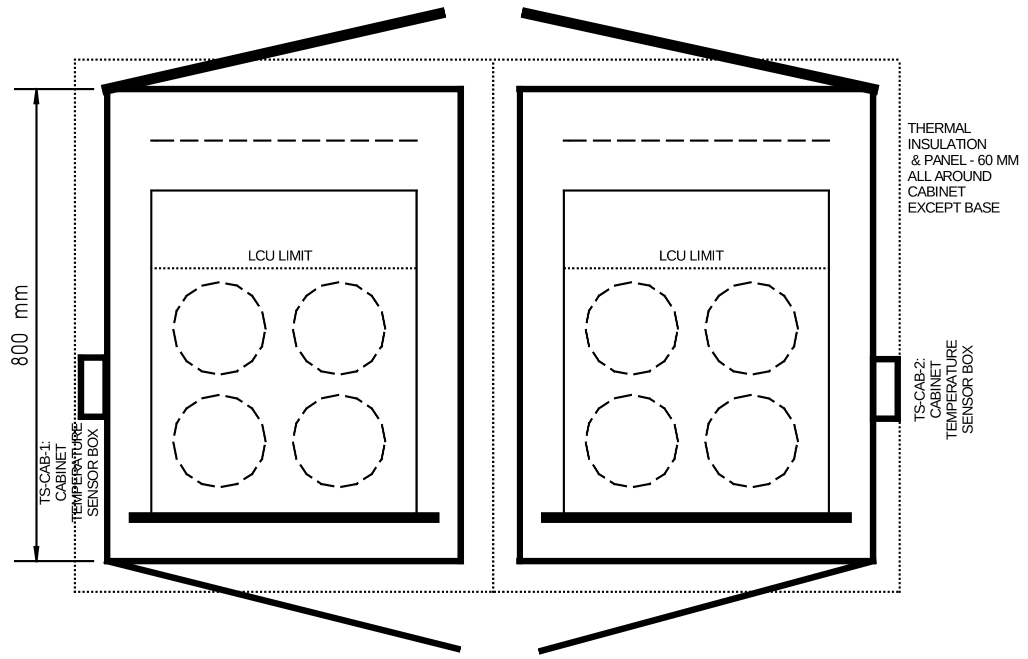


					Date	Init.		
				Drawn	24/08/00	SM		European Southern Observatory VLT - MACAO Control Electronics
				Chckd.				
				Appr.				
				Title: MACAO - CONTROL ELECTRONICS OVERVIEW				Page 1/1
2	FDR	6/3/2001	SM					
1	PDR	24/09/00	SM	File name: MACAO.skf			Document Number: VLT-SCH-ESO-15600-2270	
Issue	Revisions	Date	Init					

FRONT VIEW



TOP VIEW



Issue	Revisions	Date	Init
2	FDR	29/01/01	SM
1	PDR	28/06/00	SM

	Date	Init.
Drawn	28/06/00	SM
Chckd.		
Appr.		

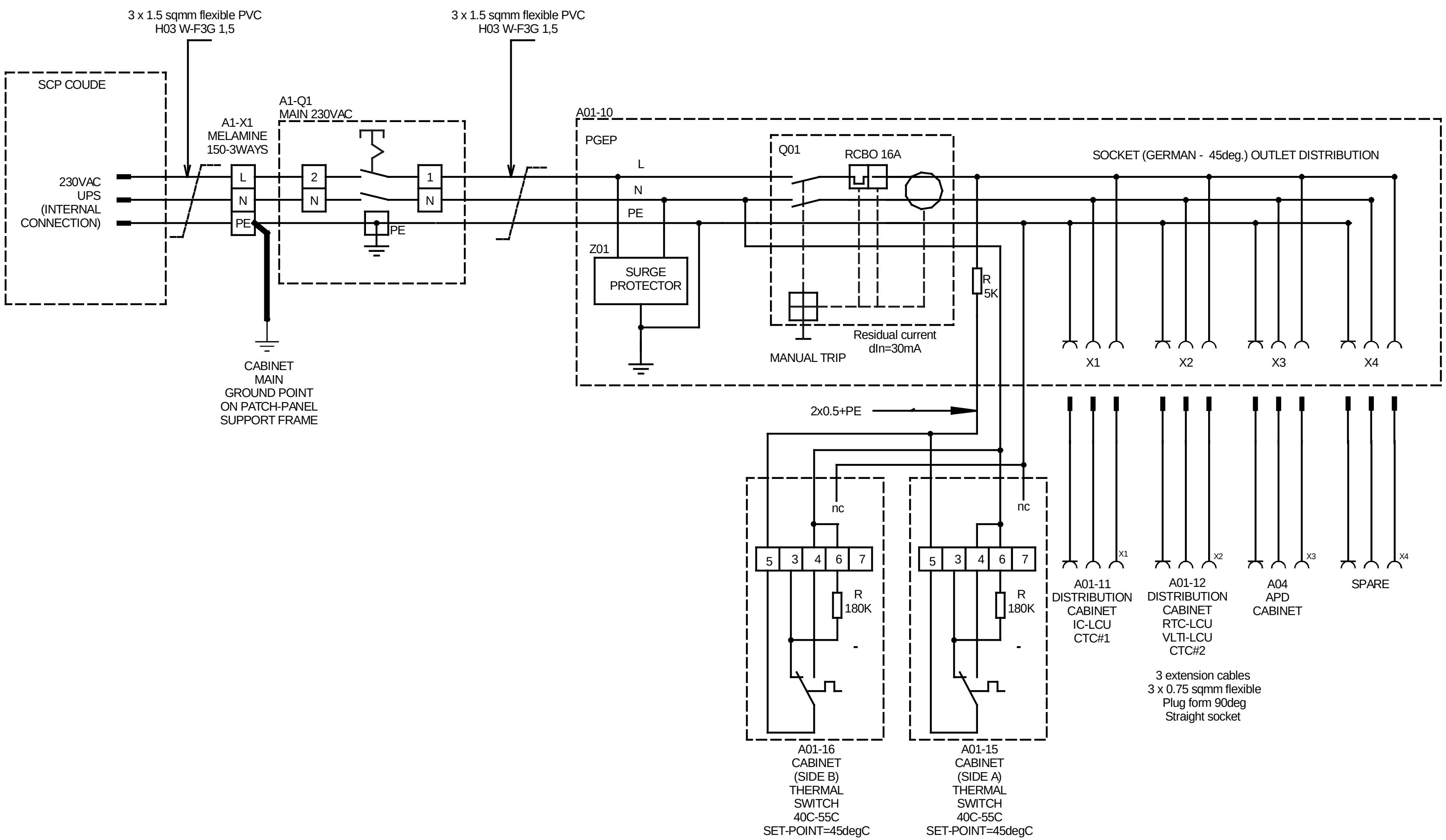
European Southern Observatory
VLT - MACAO
Control Electronics

Title: A01: CABINET LAYOUT

Page: 1/1

Document Number: VLT-SCH-ESO-15600-2270

File Name: A01-CABINET-01.skd



					Date	Init.
				Drawn	11/12/00	SM
				Chckd.		
				Appr.		
1	FDR	6/3/2001	SM	Document Number:		
Issue	Revisions	Date	Init			

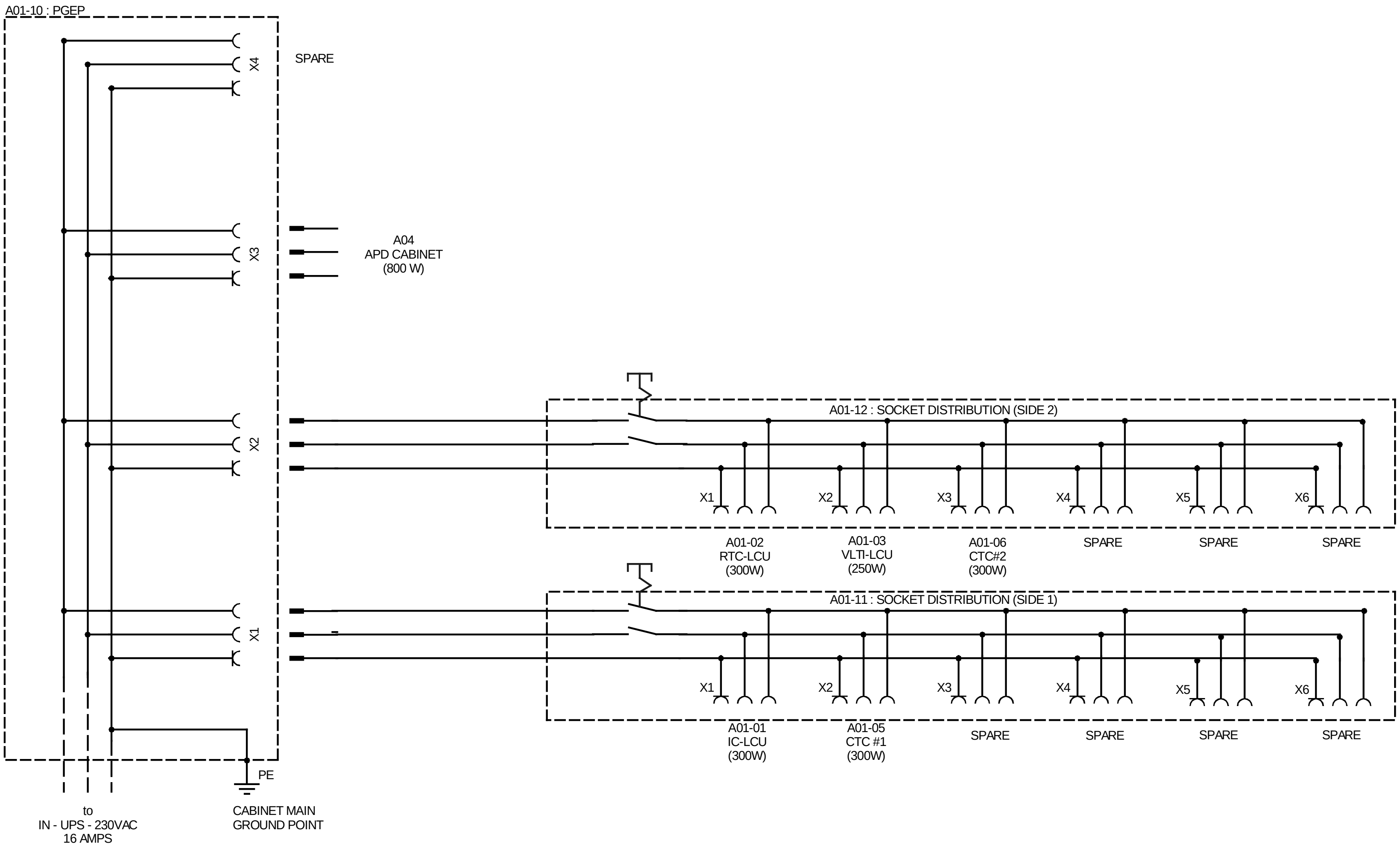
European Southern Observatory
MACAO VLTI
Control Electronics

Title: IC-CABINET POWER DISTRIBUTION
230VAC WIRING DIAGRAM

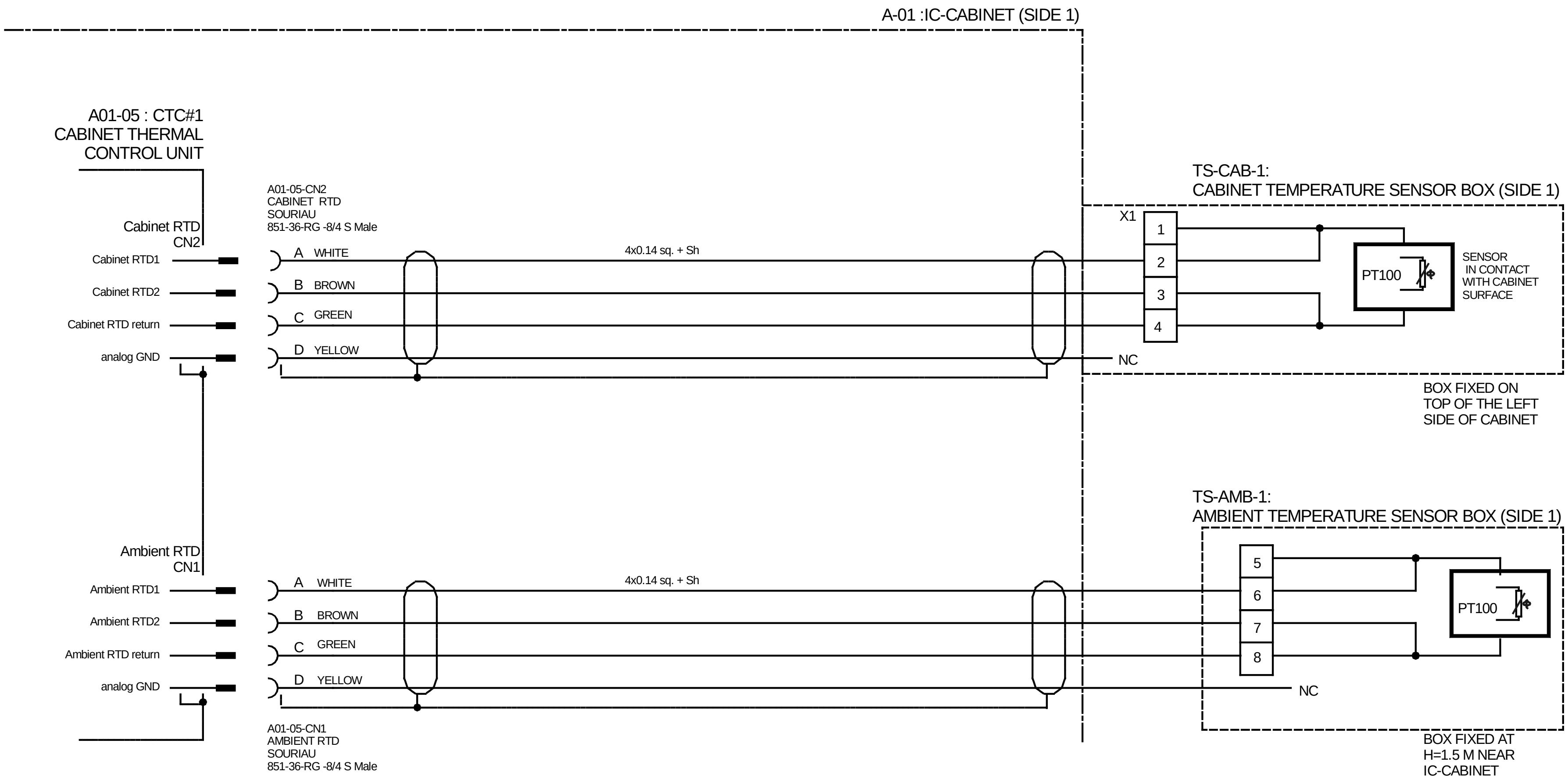
Page 1/1

Document Number: VLT-TRE-ESO-15600-2270

File Name: MAINS.skf



					Date	Init.		
				Drawn	11/12/00	SM		European Southern Observatory MACAO-VLTI Control Electronics
				Chckd.				
				Appr.				
				Title:	IC-CABINET POWER DISTRIBUTION 230VAC WIRING DIAGRAM			Page 1/1
1	FDR	06/03/01	SM	Document Number:	VLT-TRE-ESO-15600-2270			File Name: MAINS-2.skf
Issue	Revisions	Date	Init					



Issue	Revisions	Date	Init
1	FDR	11/12/00	SM

	Date	Init.
Drawn	11/12/00	SM
Chckd.		
Appr.		

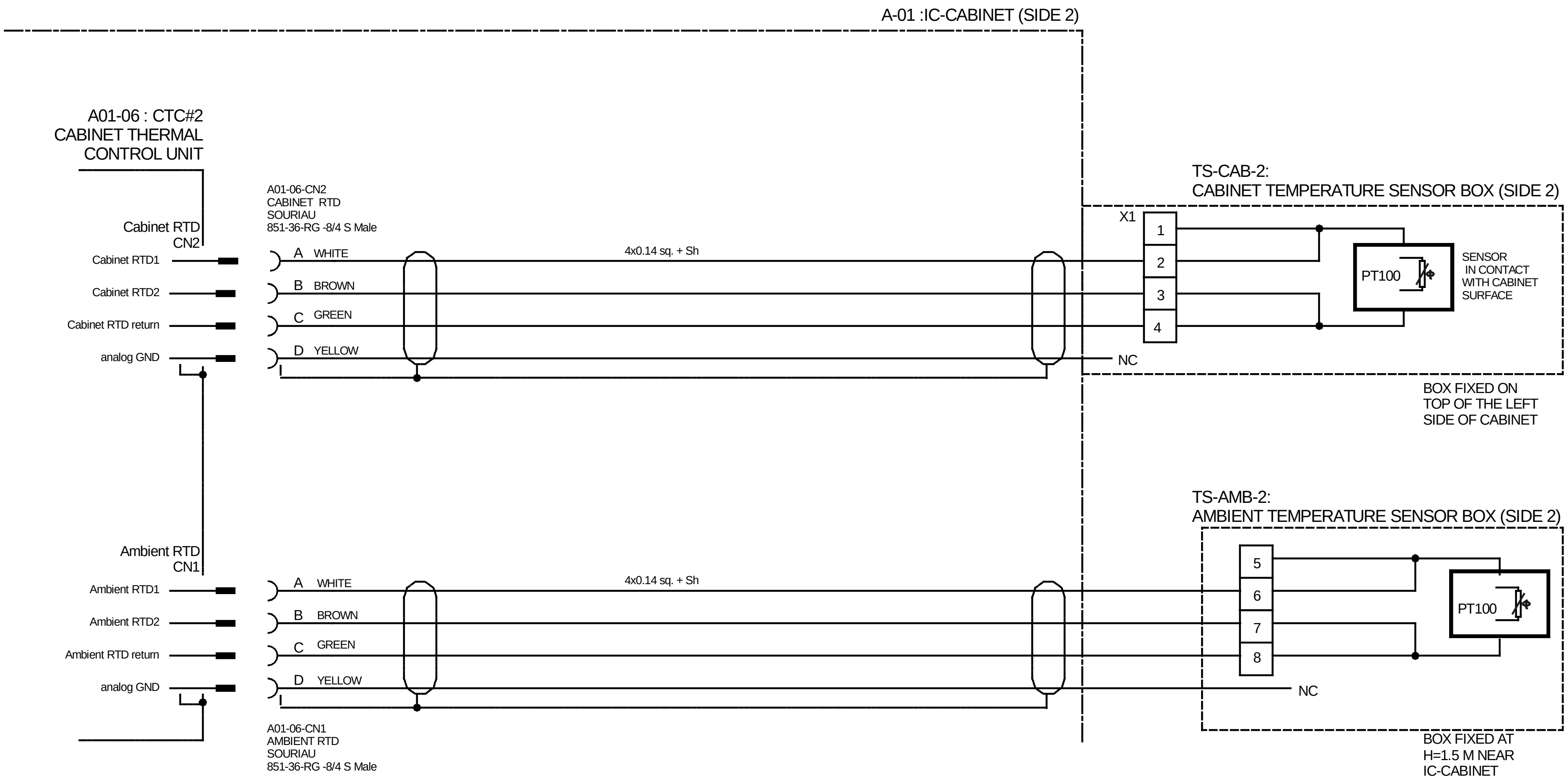
European Southern Observatory
MACAO-VLTI
Control Electronics

Title: A01-05: Cabinet Thermal Control
Temperature sensors - Wiring diagram

Page: 1/1

Drawing Number: VLT-TRE-ESO-15600-2270

File Name: PT100.skd



Issue	Revisions	Date	Init		Date	Init.
1	FDR	11/12/00	SM	Drawn	11/12/00	SM
				Chckd.		
				Appr.		

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Control Electronics

Title: A01-06: Cabinet Thermal Control
Temperature sensors - Wiring diagram

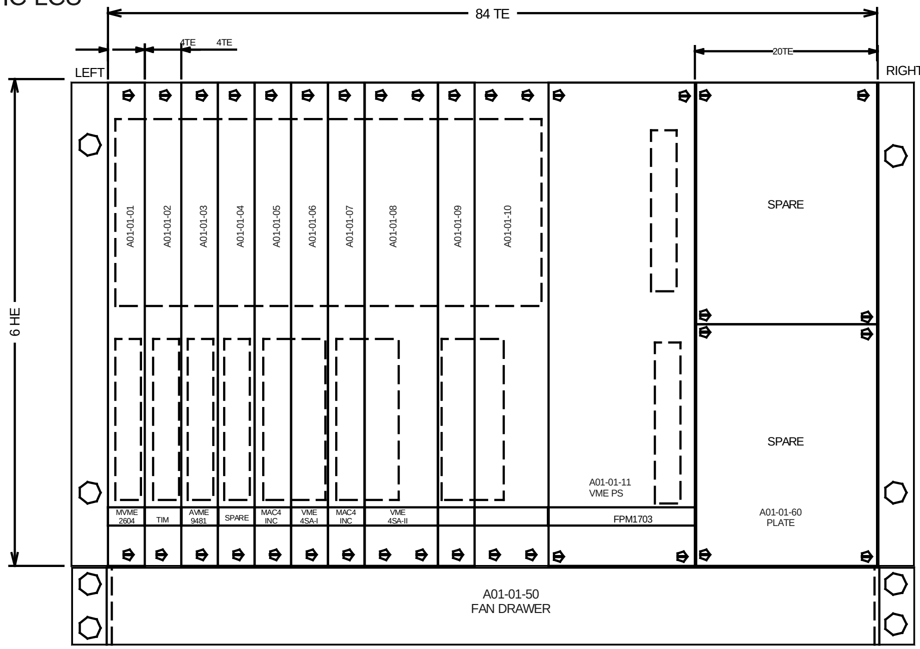
Page: 1/1

Drawing Number: VLT-TRE-ESO-15600-2270

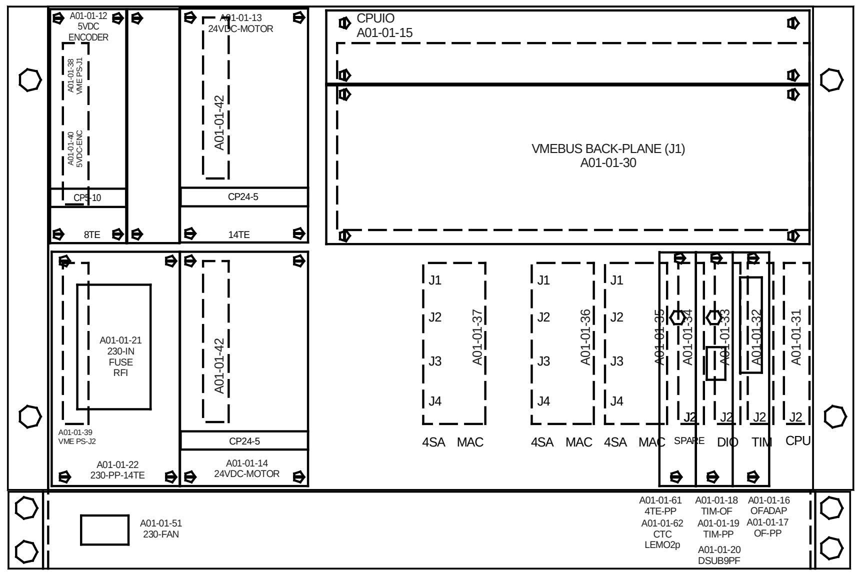
File Name: PT100B.skd

A01-01
IC-LCU

FRONT SIDE VIEW



REAR SIDE VIEW



LEGENDE:

RACK: VERO - KM6-II - 420mm deepth (Config= Type 4)
VMEBUS: 12 SLOTS - ACTIVE TERMINATION - AUTO DAISY CHAIN
CPUIO: TRANSITION MODULE MVME 712M
CPU: MVME2604 - MOTOROLA with CONTROL LAN CONNECTION
TIM: TIME INTERFACE VME MODULE
MAC4/INC: MACCON - MOTION CONTROLLER for INC ENCODER
VME4SA: C SERVO-AMPLIFIER
AVME9481: ACROMAG DIGITAL INPUT/OUTPUT BOARD
SP: SPARE
VME PS: KNIEL FPM 300W FOR VME BUS SUPPLY
24V MOTOR: KNIEL CP24-5
5V ENCODER: KNIEL CP5-10
CPU-PP: PATCH PANEL FOR LAN F.O
TIM-PP: PATCH PANEL FOR TRS F.O + RS422
FAN DRAWER: 3X FAN's PAPST HIGH EFFICIENCY

Issue	Revisions	Date	Init
3	FDR	21/11/00	SM
2	AFC	27/10/00	SM
1	PDR	28/09/00	SM

	Date	Init.
Drawn	28/06/00	SM
Chckd.		
Appr.		

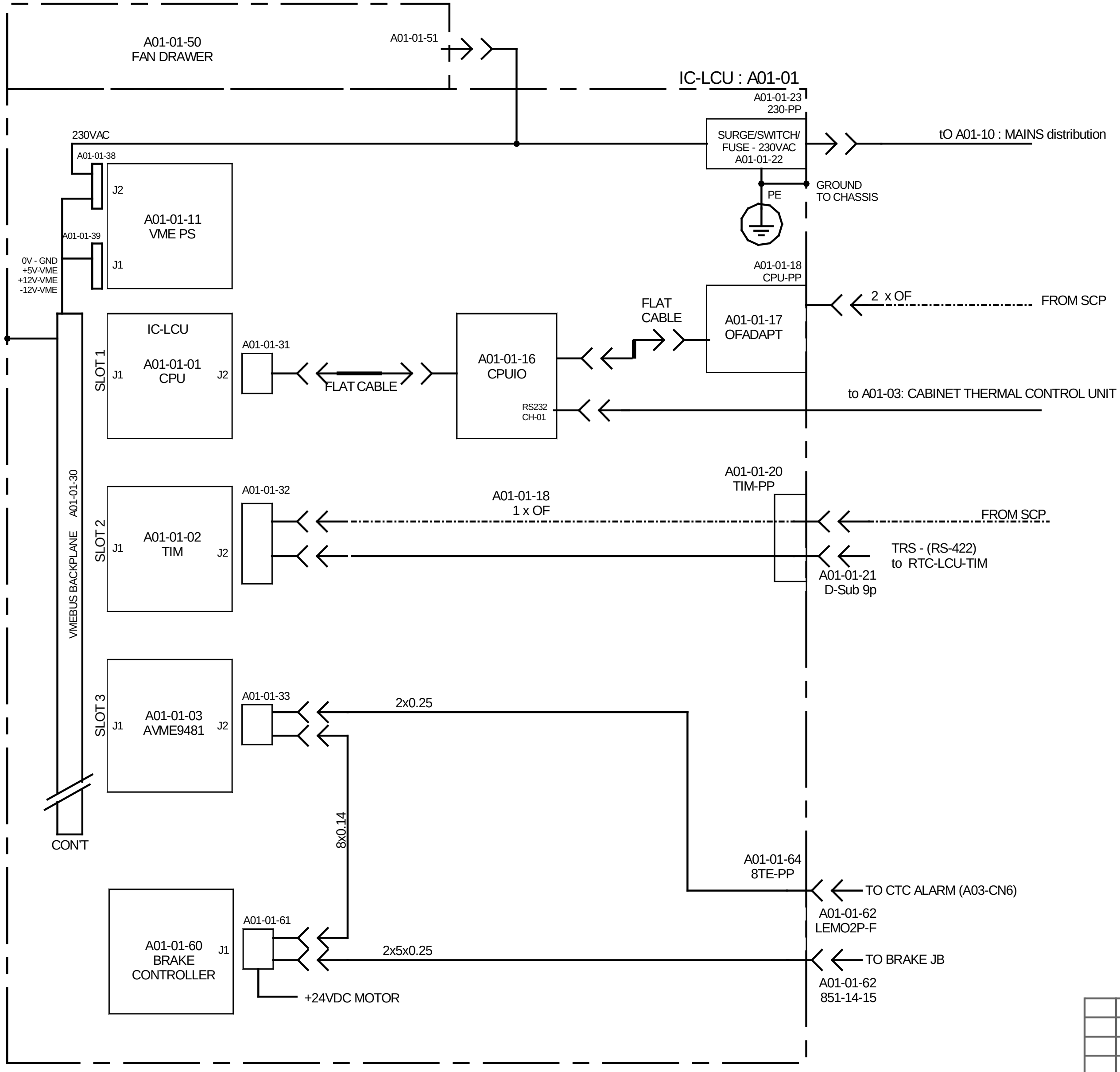
European Southern Observatory
MACAO-VLTI
Control Electronics

Title: A01-01 : IC LCU
RACK LAYOUT

Page 1/1

Document Number: VLT-SCH-ESO-15600-2270

File Name: IC-LCU-r3.skd



Issue	Revisions	Date	Init
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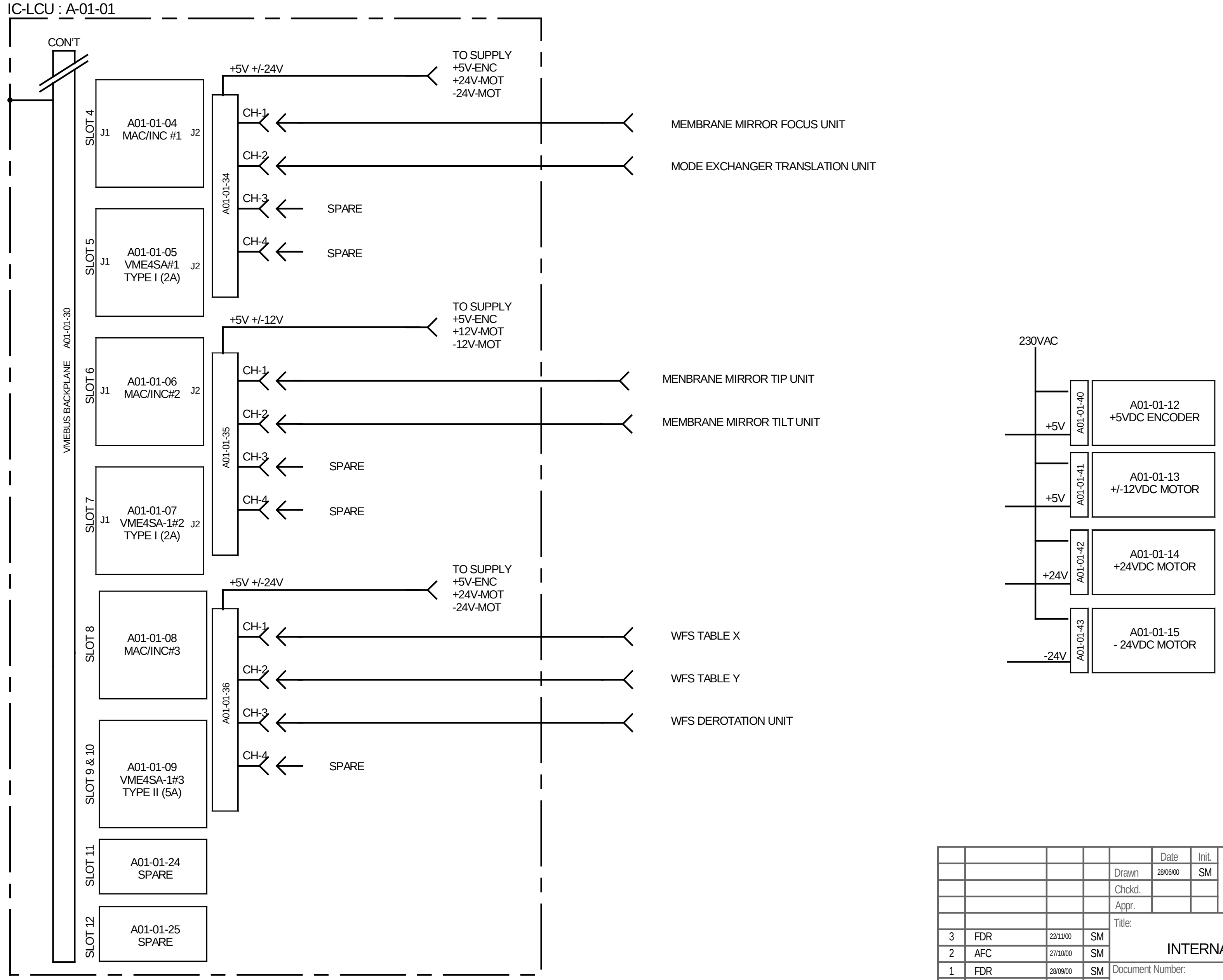
2	AFC	27/10/00	SM
1	PDR	28/06/00	SM

	Date	Init.
Drawn	28/06/00	SM
Chckd.		
Appr.		

European Southern Observatory
MACAO-VLTI
Control Electronics

Title:	Page
A01-01: IC-LCU INTERNAL WIRING DIAGRAM (Part 1)	1/1
Document Number: VLT-SCH-ESO-15600-2270	File Name: IC-LCU-01_r3.skd



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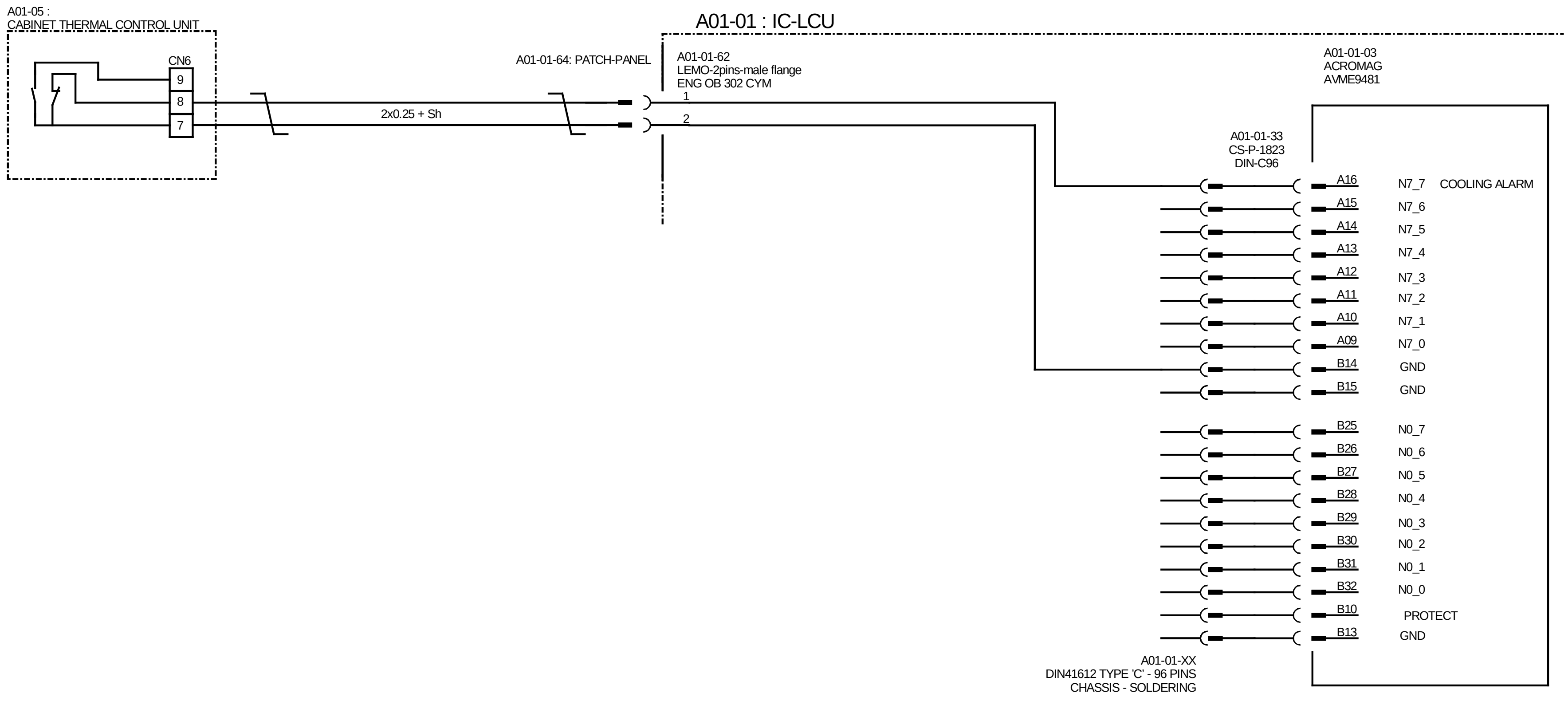
				Drawn	28/06/00	SM		European Southern Observatory
				Chckd.				MACAO - VLTI
				Appr.				Control Electronics
				Title: A01-01: IC-LCU INTERNAL WIRING DIAGRAM (Part 2)				Page 1/1
3	FDR	22/11/00	SM					
2	AFC	27/10/00	SM					
1	FDR	28/09/00	SM	Document Number: VLT-SCH-ESO-15600-2270				File Name: IC-LCU-02_r3.skd
Issue	Revisions	Date	Init					



	Date	Init.
Drawn	14/11/00	SM
Chckd.		
Appr.		

European Southern Observatory
MACAO-VLTI
Control Electronics

Title: A01-05: CTC#1 - ALARM
Digital I/O - Wiring diagram

Page: 1/1

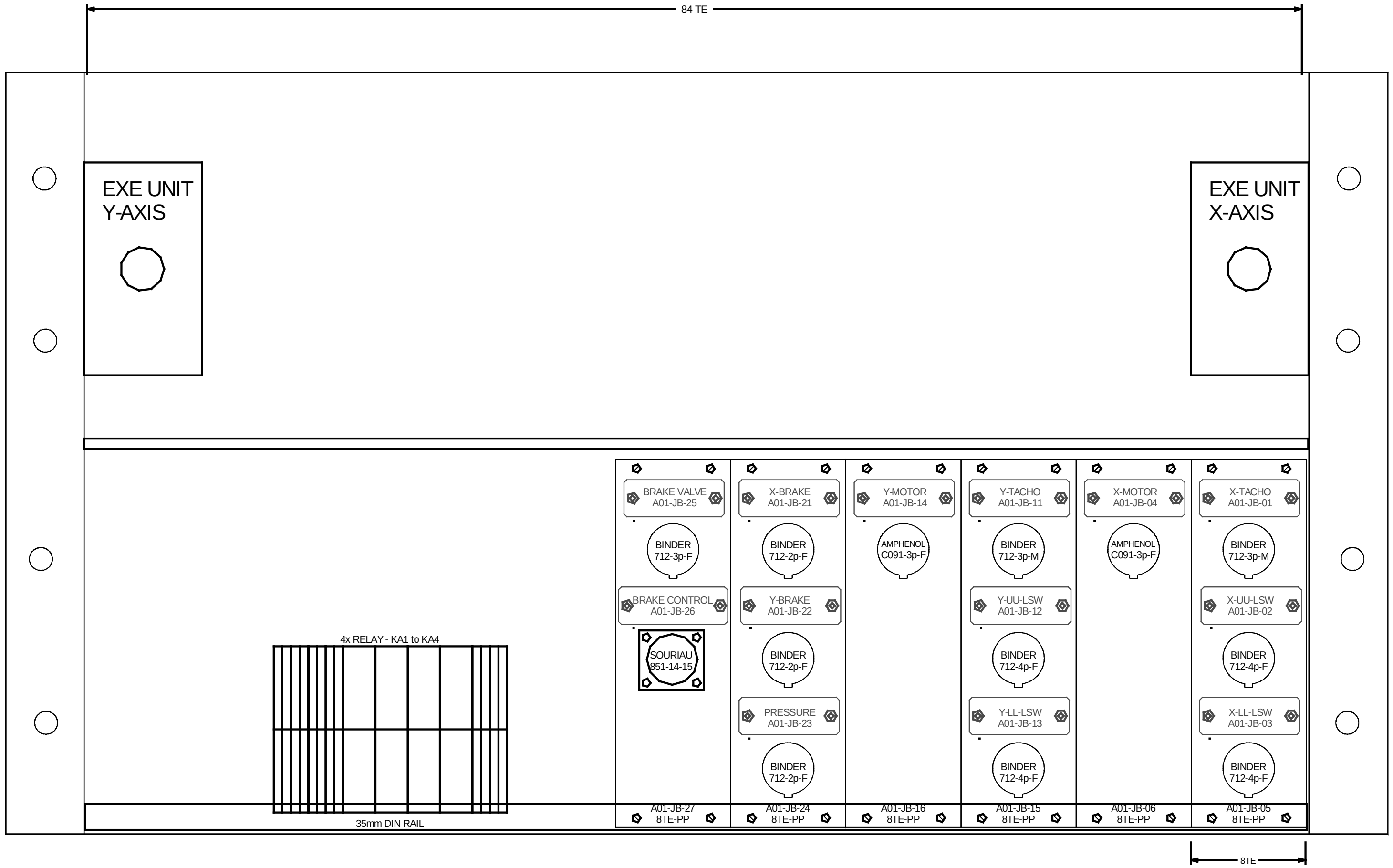
Issue	Revisions	Date	Init
1	FDR	29/01/01	SM

Document Number: VLT-TRE-ESO-15600-2270

File Name: IC-LCU-03.skd

REAR SIDE VIEW

A01-JB
WFS XY-TABLE



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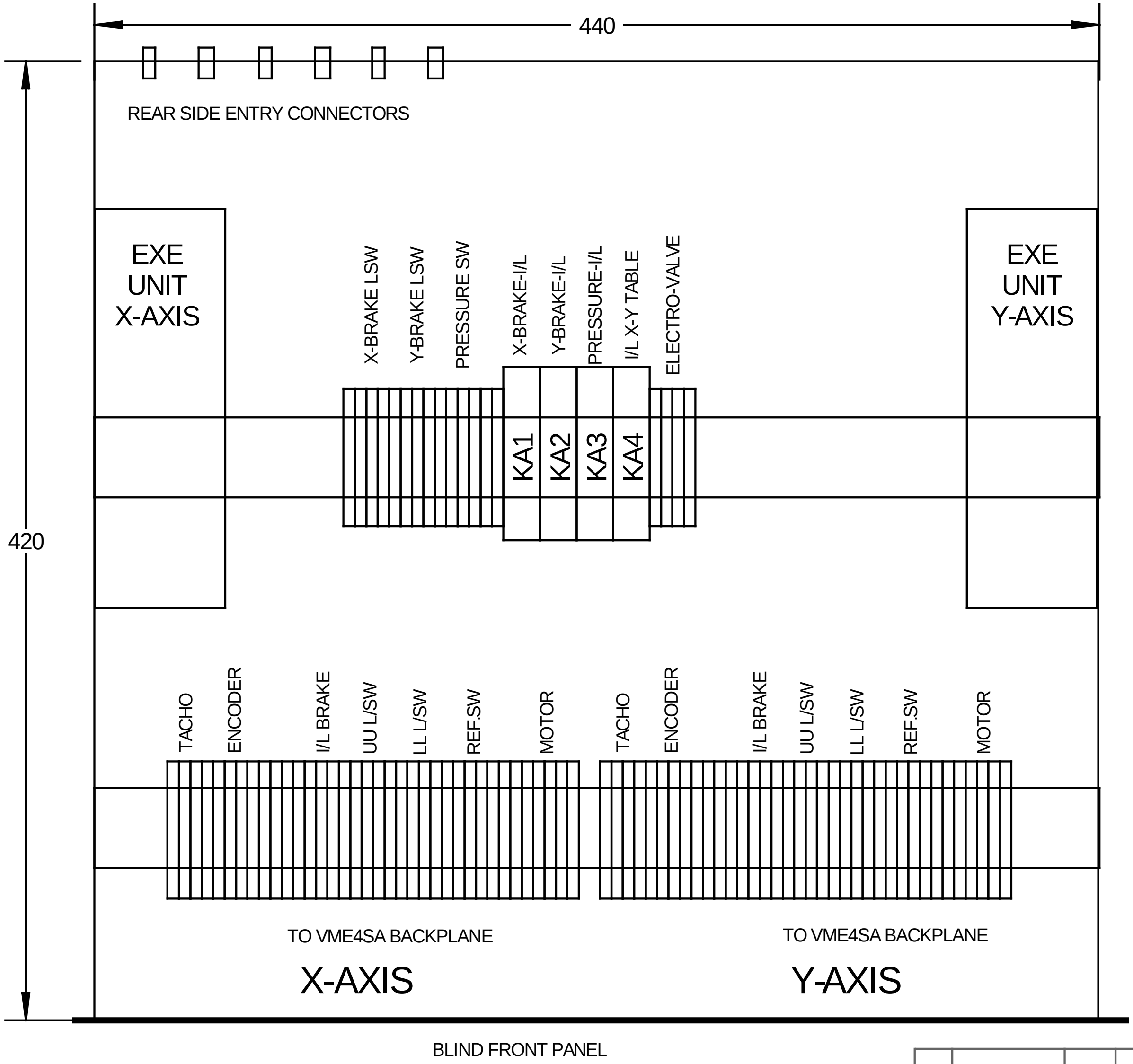
				Chckd.			
				Appr.			
				Title: WFS-XY TABLE JUNCTION BOX LAYOUT			Page 1/1
1	AFC	02/03/01	SM	Document Number: VLT-TRE-ESO-15600-2270			File Name: TABLE-JB.skf
Issue	Revisions	Date	Init				



Issue	Revisions	Date	Init
1	AFC	27/10/00	SM
1	PDR	28/09/00	SM

	Date	Init.
Drawn	28/06/00	SM
Chckd.		
Appr.		

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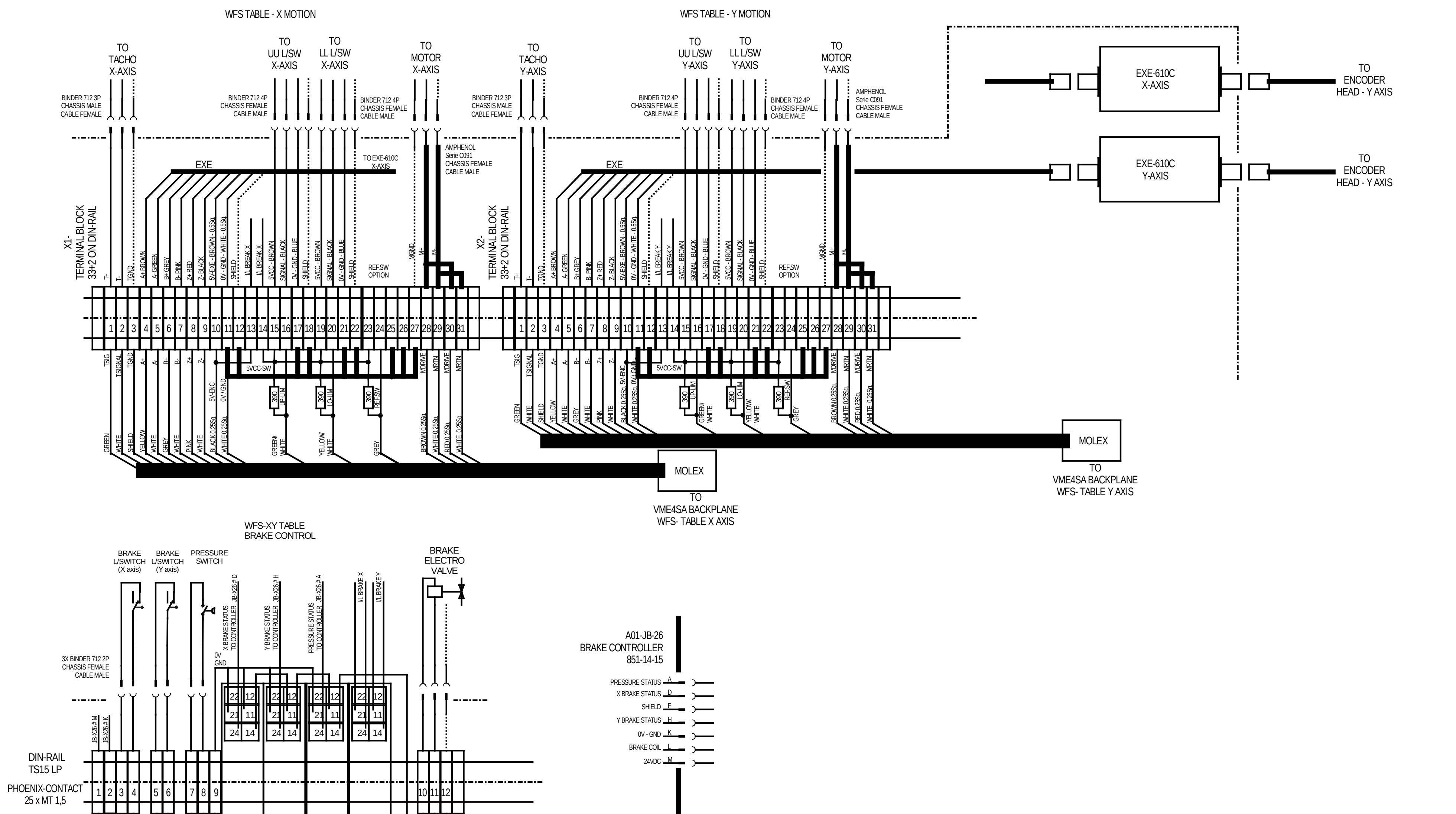
Control Electronics

Title: WFS-TABLE : A01-JB JUNCTION PANEL INTERNAL LAYOUT

Page 1/1

Document Number: VLT-SCH-ESO-15600-2270

File Name: A01-JB.skd



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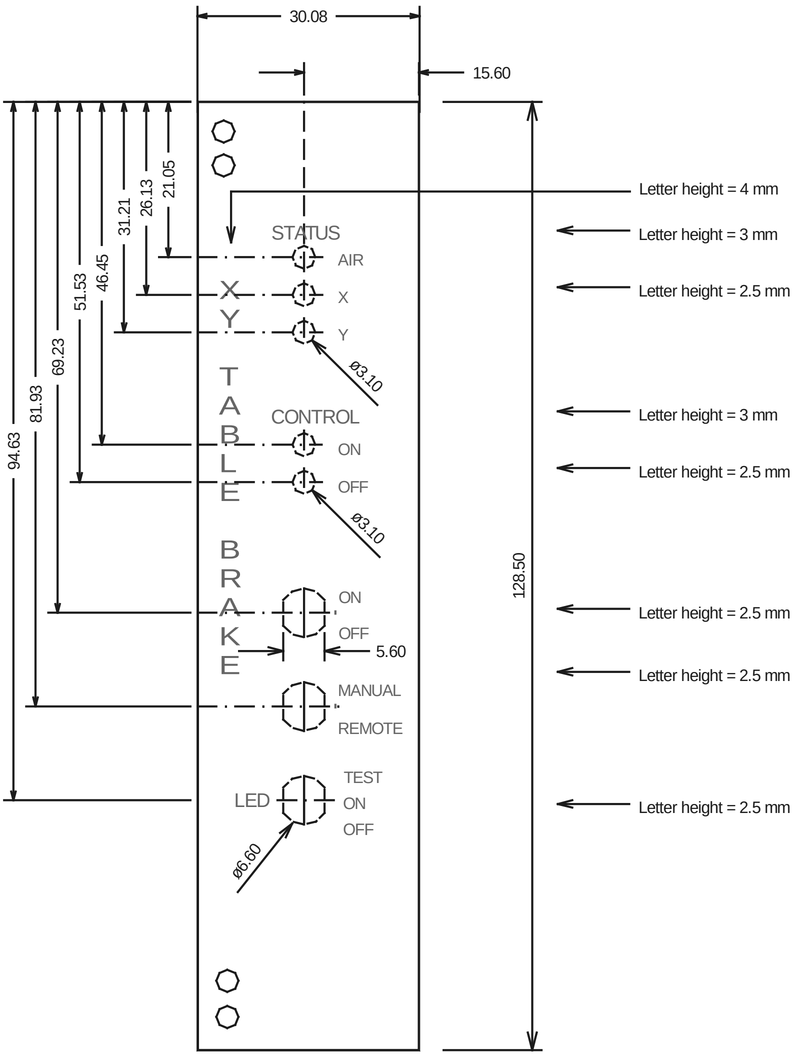
				Drawn	31/01/01	SM			
				Chckd.					
				Appr.					
				Title: WFS X-Y TABLE INTERLOCK WIRING DIAGRAM					Page 1/1
1	FDR	02/03/01	SM	Drawing Number: VLT-TRE-ESO-15600-2270				File Name: TERM.skd	
Issue	Revisions	Date	Init						



Notes:

Front plate to be used is for VERO KM6-II cassette - 3HE - 6TE - type 953-236298D

Tolerances: +/- 0.05 mm

All letters and border in BLACK color. Normalised font characters
Dashed lines are only to indicate the component dimension

6-Legende

3-Dimmension

1-Cartouche

Issue	Revisions	Date	Init
1	FDR	10/1/01	SM

	Date	Init.
Drawn	10/1/01	SM
Chckd.		
Appr.		

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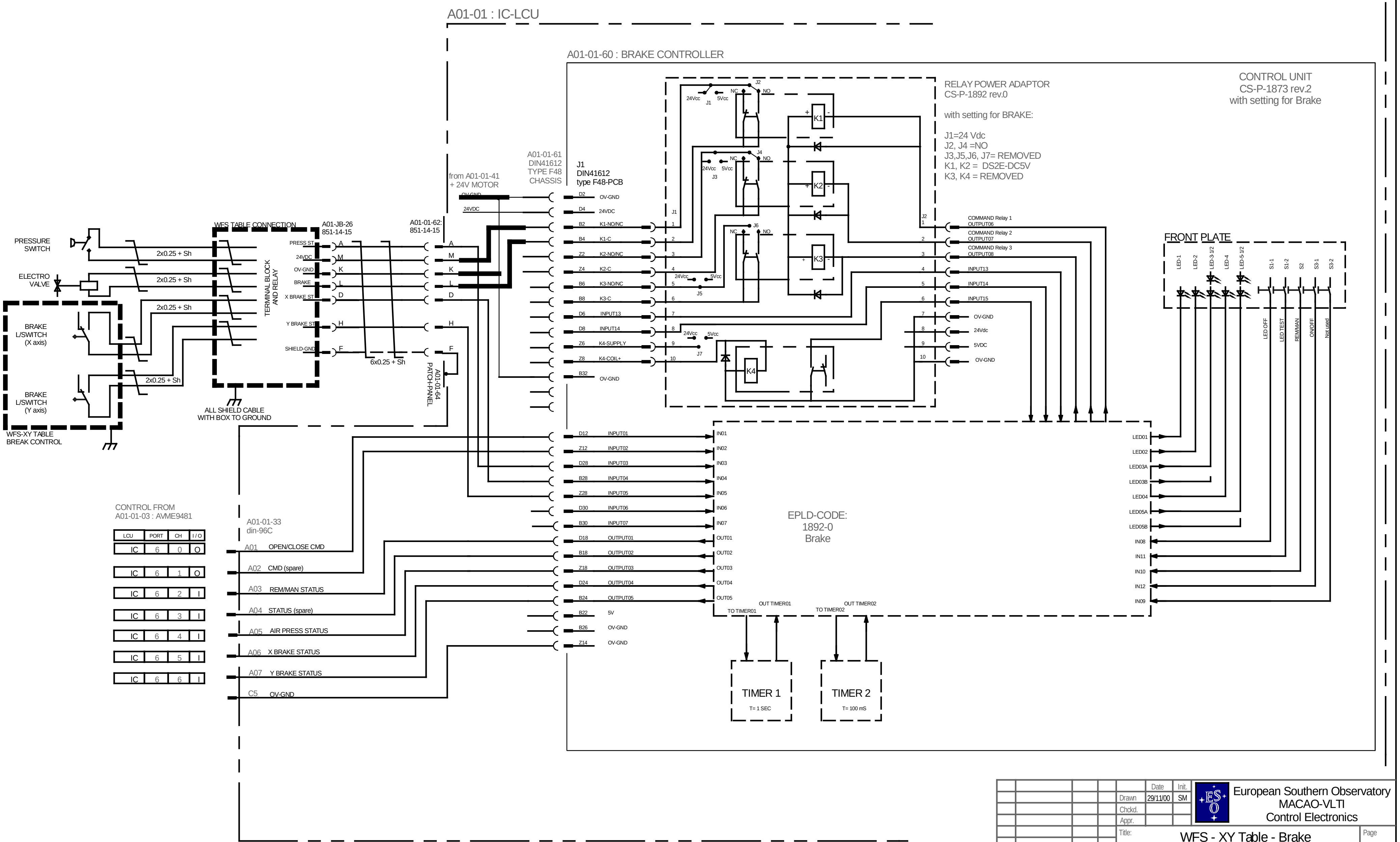
Control Electronics

Title: WFS-XY TABLE: BRAKE CONTROLLER UNIT
FRONT PANEL LAYOUT

Page 1/1

Drawing Number: VLT-TRE-ESO-15600-2270

File Name: A01-01-70-FP.skd



LCU	PORT	CH	I / O
IC	6	0	O
IC	6	1	O
IC	6	2	I
IC	6	3	I
IC	6	4	I
IC	6	5	I
IC	6	6	I

	Date	Init.
Drawn	29/11/00	SM
Chckd.		
Appr.		

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Control Electronics

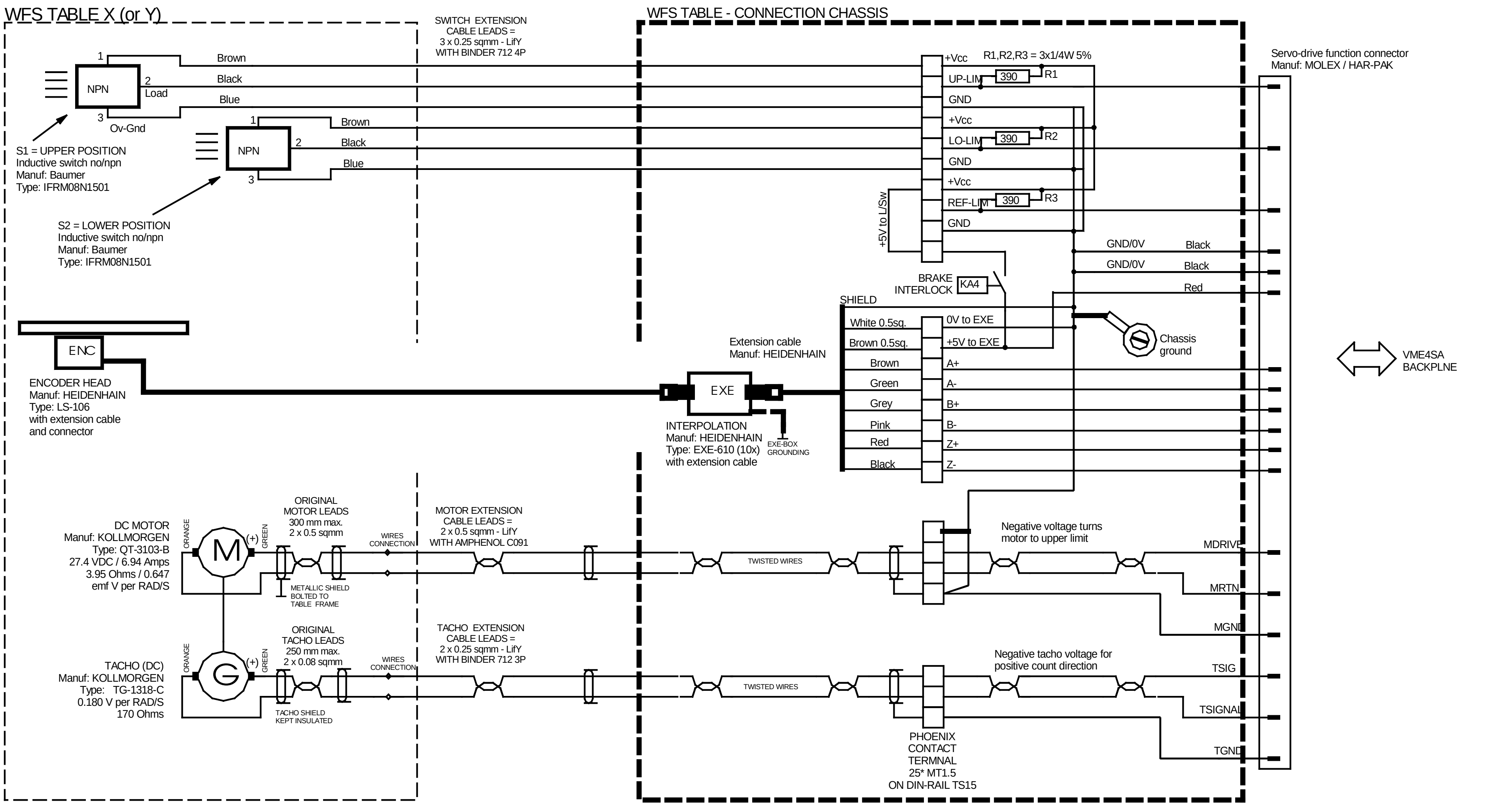
Title: WFS - XY Table - Brake Wiring diagram

Page 1/1

Issue	Revisions	Date	Init
1	FDR	6/3/2001	SM

Document Number: VLT-TRE-ESO-15600-2270

File Name: WFS-XY-BRAKE.skd



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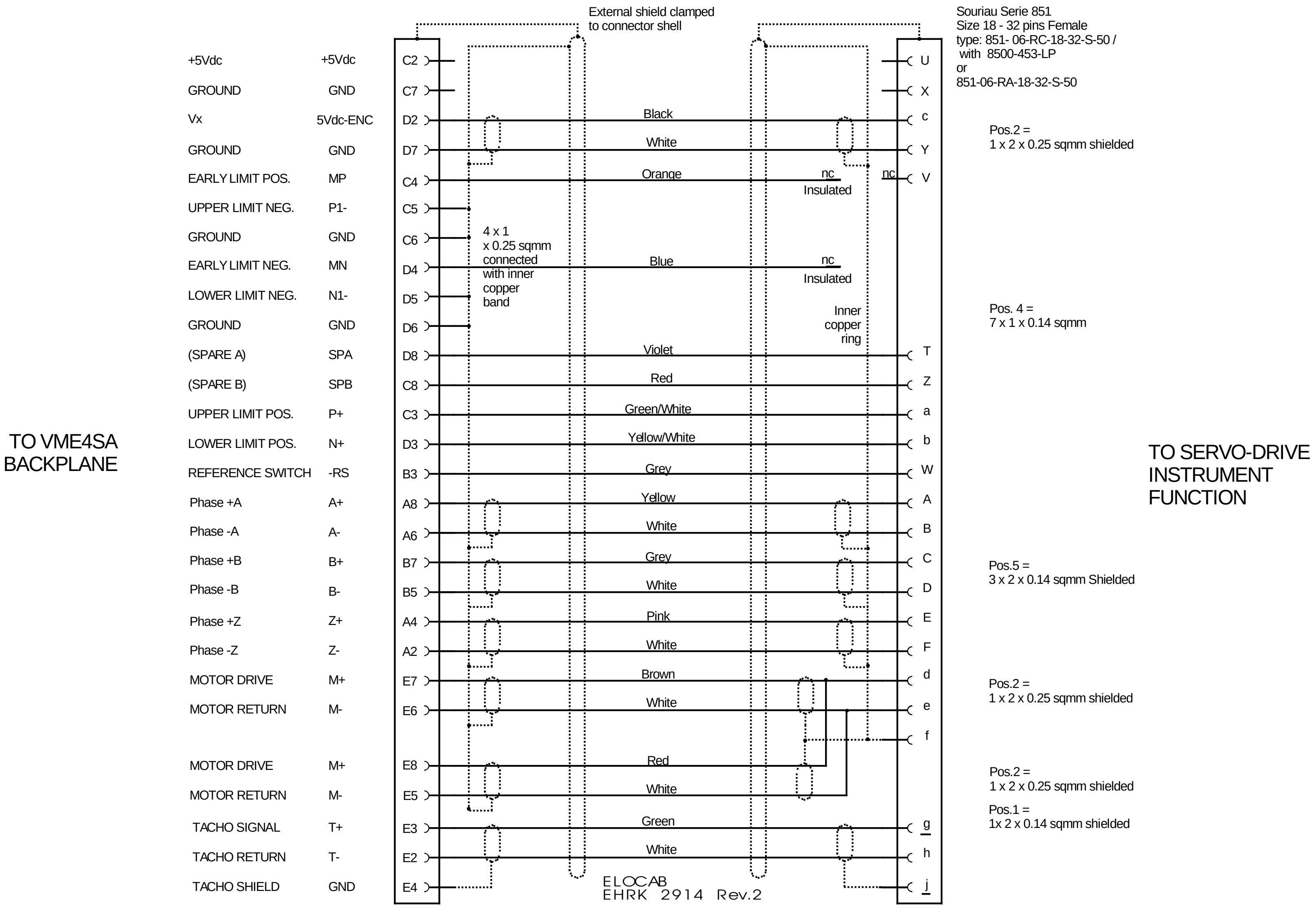
				Chckd.				
				Appr.				
				Title: WFS X-Y TABLE WIRING DIAGRAM				Page 1/1
1	FDR	01/02/01	SM	Drawing Number: VLT-SCH-ESO-15600-2270				File Name: FM-WFS-XY.skd
Issue	Revisions	Date	Init					



Notes:
1) Cable identification on both sides.
2) External and all internal shielding (except for tacho) clamped to the connector body with connection to pin " f ".
3) Keep tacho shielding isolated from other and connected it to pin " j ".

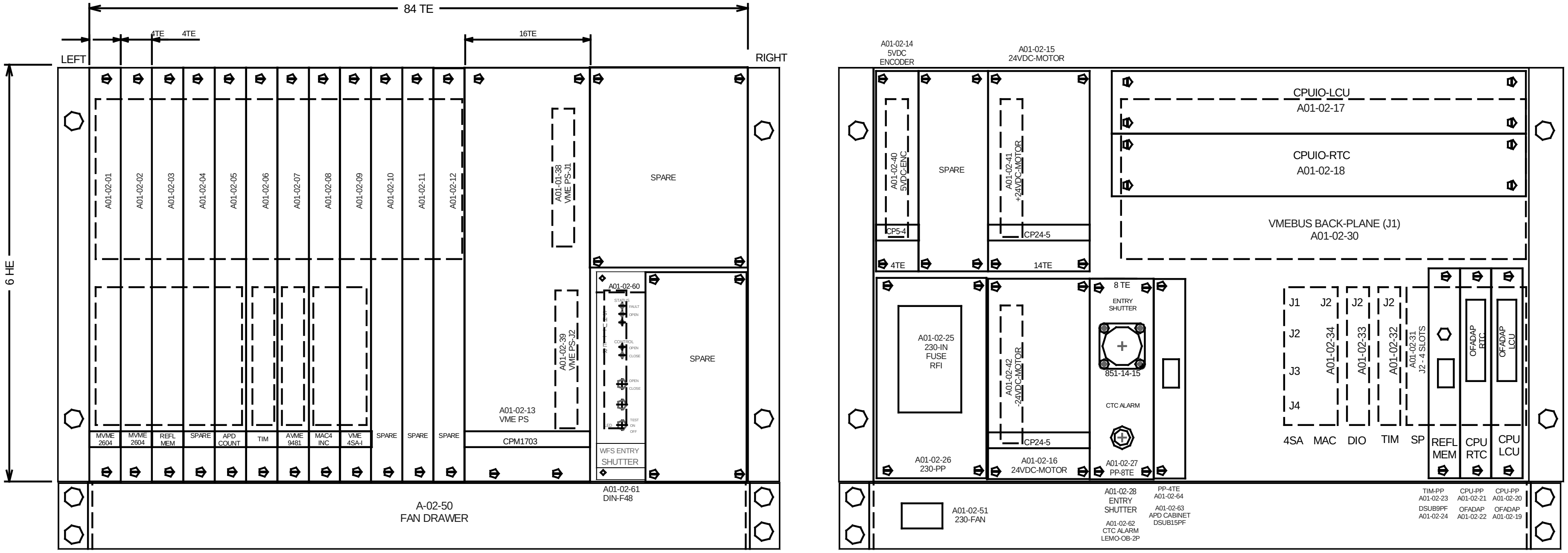
41-PIN FEMALE - CRIMP TYPE
HAR-PAK type: 07-511-04-0000
or
MOLEX OmniGrid 2.5

		Date	Init.	European Southern Observatory MACAO-VLTI Control Electronics		
	Drawn	28/11/00	SM			
	Chckd.					
	Appr.					
Title:	MOTION FUNCTION CABLE WIRING DIAGRAM				Page	1/1
1	FDR	28/11/00	SM			
Issue	Revisions	Date	Init	Document Number: VLT-TRE-ESO-15600-2270	File Name: FM-CAB.skd	

A01-02
RTC-LCU

FRONT SIDE VIEW

REAR SIDE VIEW



LEGENDE:

RACK: VERO - KM6-II - 420mm deepth (Config= Type 4)
J1: VMEBUS: 10 SLOTS - ACTIVE TERMINATION - AUTO DAISY CHAIN
J2: VMEBUS 4 SLOTS
CPUIO: TRANSITION MODULE MVME 712M
MVME2604 - MOTOROLA with CONTROL LAN CONNECTION
REFL MEM: VMIC - VMIVME5576
TIM: ESO - TIME INTERFACE VME MODULE
APD COUNT: 60 Ch COUNTER BOARD
MAC4/INC: MACCON - MOTION CONTROLLER
VME4SA: ESO - DC SERVO-AMPLIFIER TYPE 1 (2Amps)
AVME9481: ACROMAG - DIGITAL INPUT/OUTPUT BOARD
SP: SPARE
VME PS: KNIEL FPM 300W FOR VME BUS SUPPLY
24V MOTOR: KNIEL CP24-5
5V ENCODER: KNIEL CP5-4
CPU-PP: PATCH PANEL FOR LAN F.O
TIM-PP: PATCH PANEL FOR TRS F.O + RS422
FAN DRAWER: 3X FAN's PAPST HIGH EFFICIENCY

		Date	Init.
Drawn		28/06/00	SM
Chckd.			
Appr.			

European Southern Observatory
MACAO-VLTI
Control Electronics

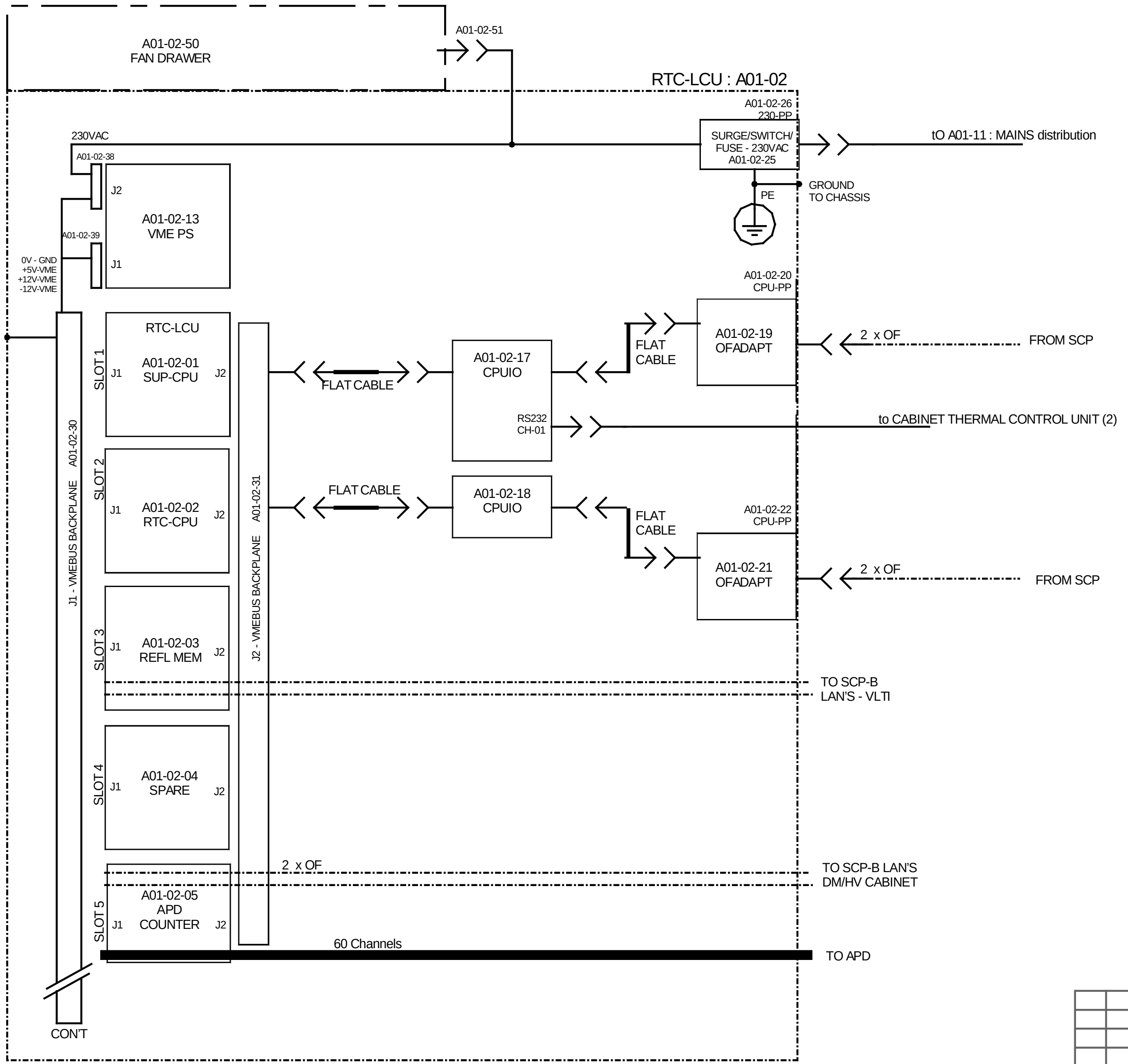
Issue	Revisions	Date	Init
3	FDR	22/01/01	SM
2	AFC	27/10/00	SM
1	PDR	28/09/00	SM

Title: A01-02 : RTC LCU RACK LAYOUT

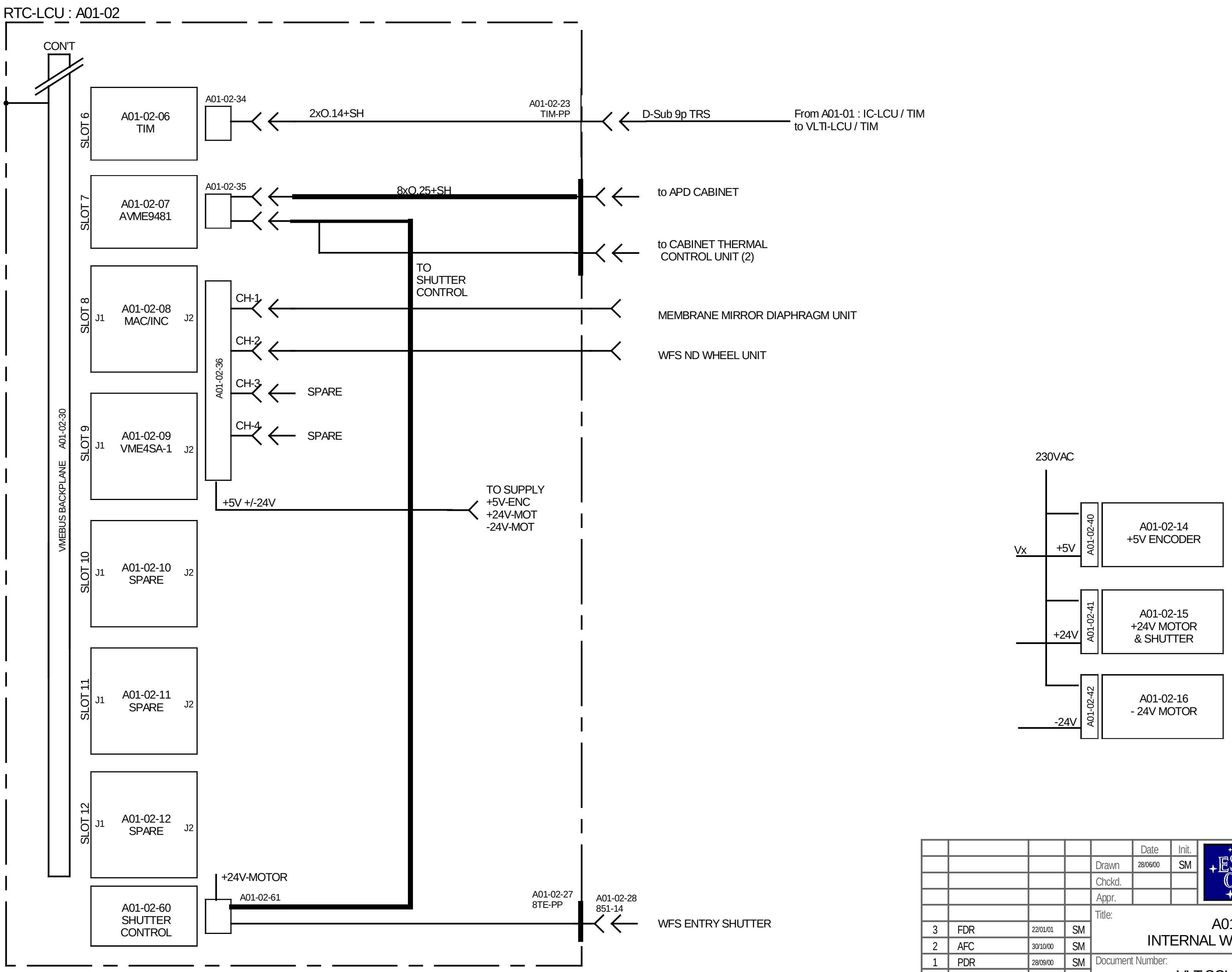
Page 1/1

Drawing Number: VLT-TRE-ESO-15600-2270

File Name: RTC-LCU_r3.skd



					Date	Init.		
				Drawn	28/06/00	SM		European Southern Observatory MACAO-VLTI Control Electronics
				Chckd.				
				Appr.				
				Title:	A01-02: RTC-LCU INTERNAL WIRING DIAGRAM (Part 1)			Page
3	FDR	6/3/2001	SM					1/1
2	AFC	30/10/00	SM					
1	PDR	28/06/00	SM	Document Number:	VLT-SCH-ESO-15600-2270		File Name:	RTC-LCU-01_r3.skd
Issue	Revisions	Date	Init					



Issue	Revisions	Date	Init
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2	AFC	30/10/00	SM
1	PDR	28/09/00	SM

	Date	Init.
Drawn	28/06/00	SM
Chckd.		
Appr.		

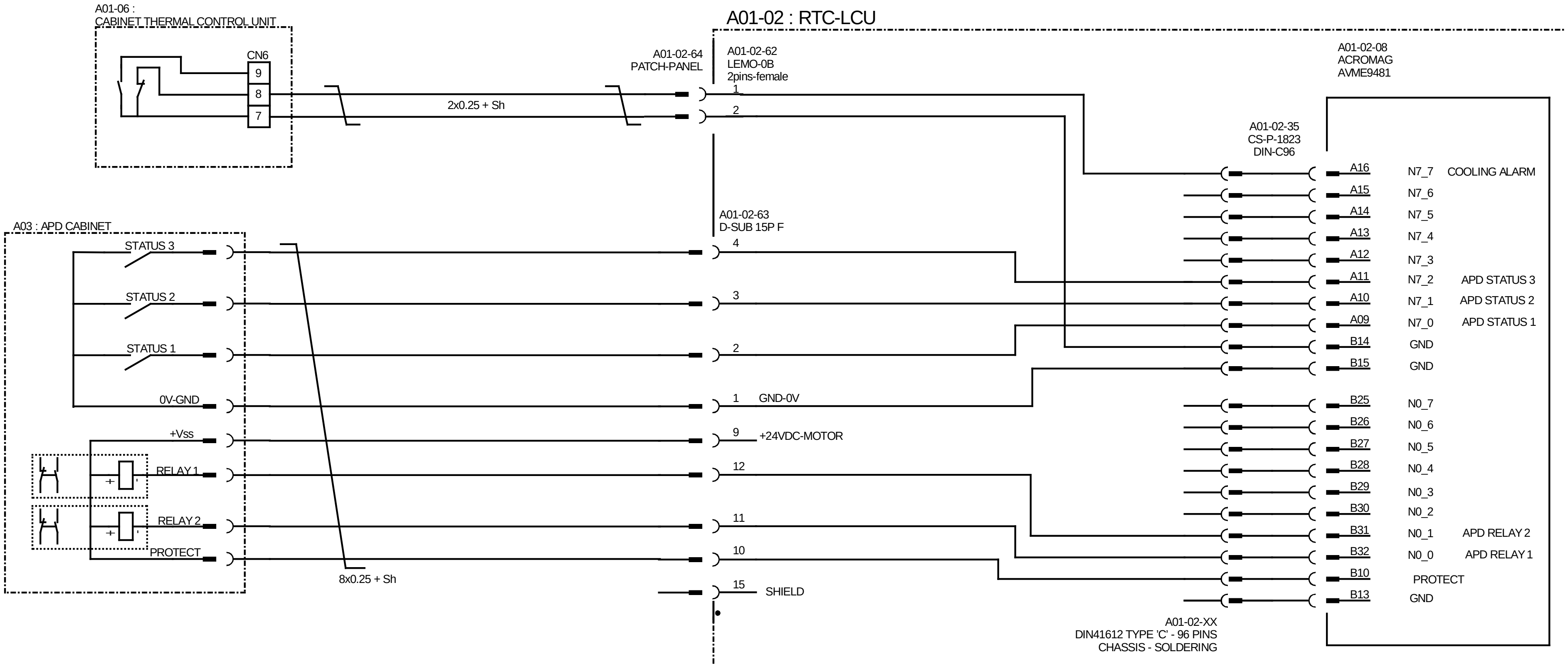
European Southern Observatory
MACAO-VLTI
Control Electronics

Title: A01-02: RTC-LCU
INTERNAL WIRING DIAGRAM (Part 2)

Page: 1/1

Document Number: VLT-SCH-ESO-15600-2270

File Name: RTC-LCU-02_r3.skd



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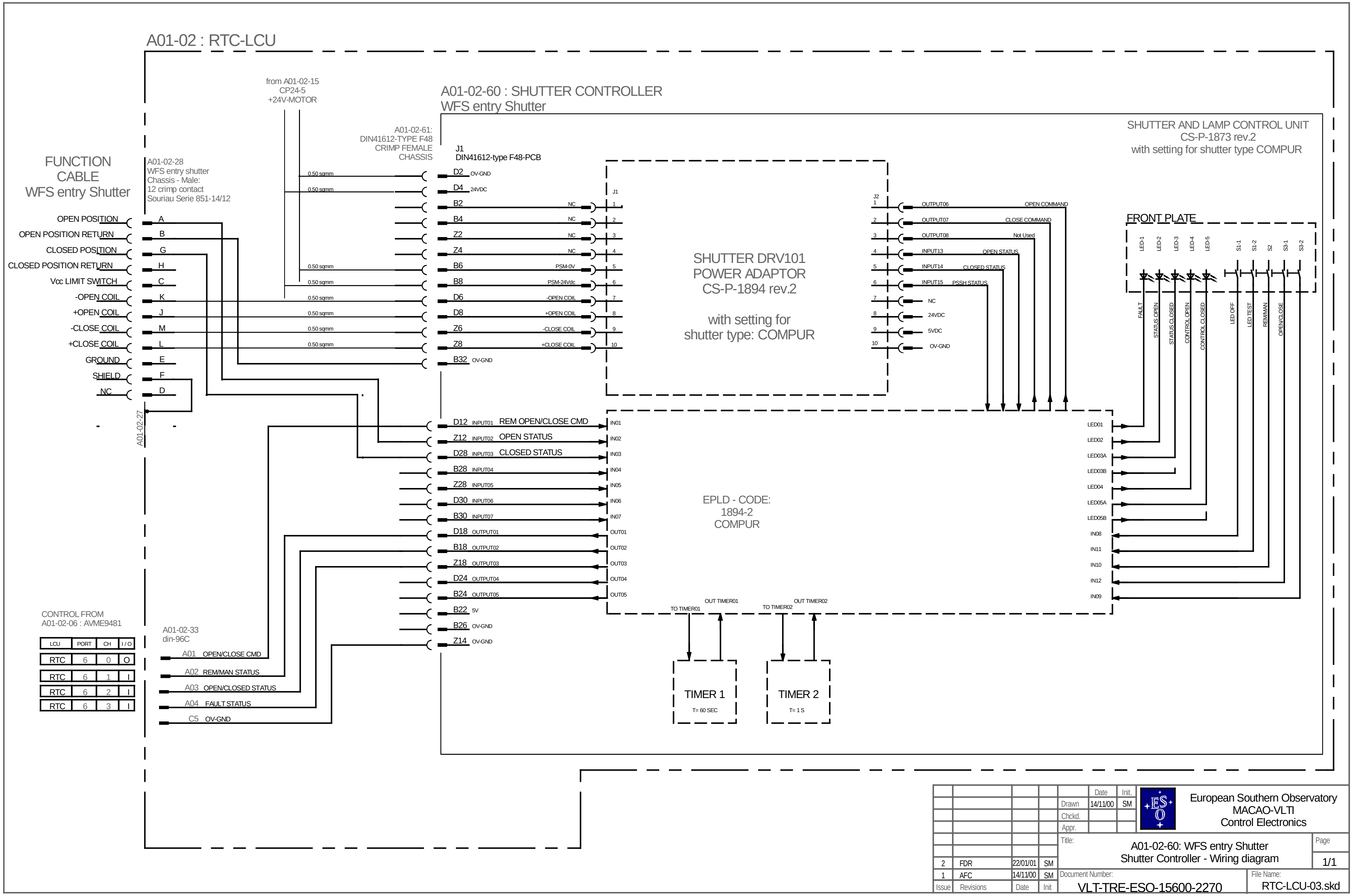
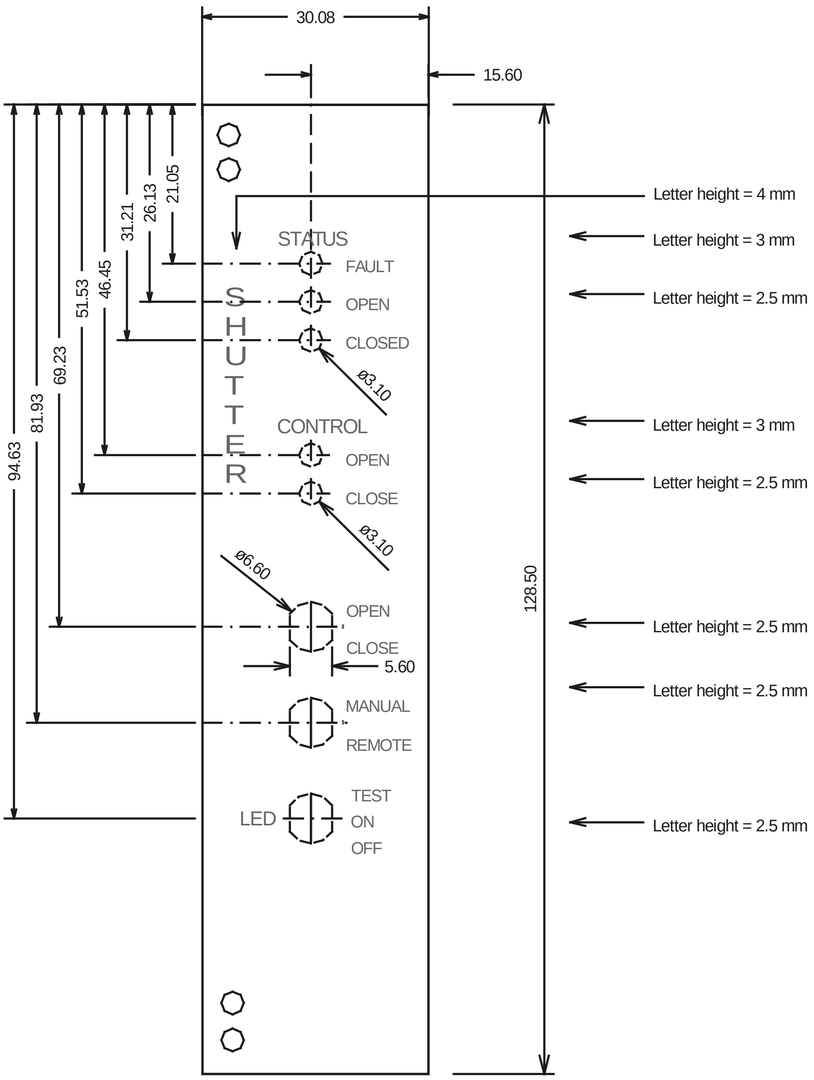
			Drawn	14/11/00	SM	
			Chckd.			
			Appr.			
			Title: APD & CTC Digital I/O - Wiring diagram			Page 1/1
1	FDR	22/01/01	SM	Document Number: VLT-TRE-ESO-15600-2270		File Name: RTC-LCU-04.skd
Issue	Revisions	Date	Init			


A01-02 : RTC-LCU
from A01-02-15
CP24-5
+24V-MOTOR
A01-02-60 : SHUTTER CONTROLLER
WFS entry Shutter
SHUTTER AND LAMP CONTROL UNIT
CS-P-1873 rev.2
with setting for shutter type COMPUR
FUNCTION
CABLE
WFS entry Shutter
A01-02-28
WFS entry shutter
Chassis - Male:
12 crimp contact
Souriau Serie 851-14/12
A01-02-61:
DIN41612-TYPE F48
CRIMP FEMALE
CHASSIS
J1
DIN41612-type F48-PCB
OPEN POSITION
OPEN POSITION RETURN
CLOSED POSITION
CLOSED POSITION RETURN
Vcc LIMIT SWITCH
-OPEN COIL
+OPEN COIL
-CLOSE COIL
+CLOSE COIL
GROUND
SHIELD
NC
A
B
G
H
C
K
J
M
L
E
F
D
A01-02-27
0.50 sqmm
D2 OV-GND
D4 24VDC
B2 NC
B4 NC
Z2 NC
Z4 NC
B6 PSM-0V
B8 PSM-24Vdc
D6 -OPEN COIL
D8 +OPEN COIL
Z6 -CLOSE COIL
Z8 +CLOSE COIL
B32 OV-GND
SHUTTER DRV101
POWER ADAPTOR
CS-P-1894 rev.2
with setting for
shutter type: COMPUR
J2
OUTPUT06 OPEN COMMAND
OUTPUT07 CLOSE COMMAND
OUTPUT08 Not Used
INPUT13 OPEN STATUS
INPUT14 CLOSED STATUS
INPUT15 PSSH STATUS
NC
24VDC
5VDC
OV-GND
FRONT PLATE
LED-1
LED-2
LED-3
LED-4
LED-5
S1-1
S1-2
S2
S3-1
S3-2
FAULT
STATUS OPEN
STATUS CLOSED
CONTROL OPEN
CONTROL CLOSED
LED OFF
LED TEST
REM/MAN
OPEN/CLOSE
D12 INPUT01 REM OPEN/CLOSE CMD
Z12 INPUT02 OPEN STATUS
D28 INPUT03 CLOSED STATUS
B28 INPUT04
Z28 INPUT05
D30 INPUT06
B30 INPUT07
D18 OUTPUT01
B18 OUTPUT02
Z18 OUTPUT03
D24 OUTPUT04
B24 OUTPUT05
B22 5V
B26 OV-GND
Z14 OV-GND
IN01
IN02
IN03
IN04
IN05
IN06
IN07
OUT01
OUT02
OUT03
OUT04
OUT05
EPLD - CODE:
1894-2
COMPUR
LED01
LED02
LED03A
LED03B
LED04
LED05A
LED05B
IN08
IN11
IN10
IN12
IN09
TO TIMER01
OUT TIMER01
TO TIMER02
OUT TIMER02
TIMER 1
T= 60 SEC
TIMER 2
T= 1 S
CONTROL FROM
A01-02-06 : AVME9481
LCU | PORT | CH | I / O
RTC | 6 | 0 | O
RTC | 6 | 1 | I
RTC | 6 | 2 | I
RTC | 6 | 3 | I
A01-02-33
din-96C
A01 OPEN/CLOSE CMD
A02 REM/MAN STATUS
A03 OPEN/CLOSED STATUS
A04 FAULT STATUS
C5 OV-GND
Date Init.
Drawn 14/11/00 SM
Chckd.
Appr.
European Southern Observatory
MACAO-VLTI
Control Electronics
Title:
A01-02-60: WFS entry Shutter
Shutter Controller - Wiring diagram
Page
1/1
2 FDR 22/01/01 SM
1 AFC 14/11/00 SM
Issue Revisions Date Init
Document Number:
VLT-TRE-ESO-15600-2270
File Name:
RTC-LCU-03.skd



Notes:

Front plate to be used is for VERO KM6-II cassette - 3HE - 6TE - type 953-236298D

Tolerances: +/- 0.05 mm

All letters and border in BLACK color. Normalised font characters
Dashed lines are only to indicate the component dimension

6-Legende

3-Dimmension

1-Cartouche

Issue	Revisions	Date	Init
1	FDR	10/1/01	SM

	Date	Init.
Drawn	10/1/01	SM
Chckd.		
Appr.		

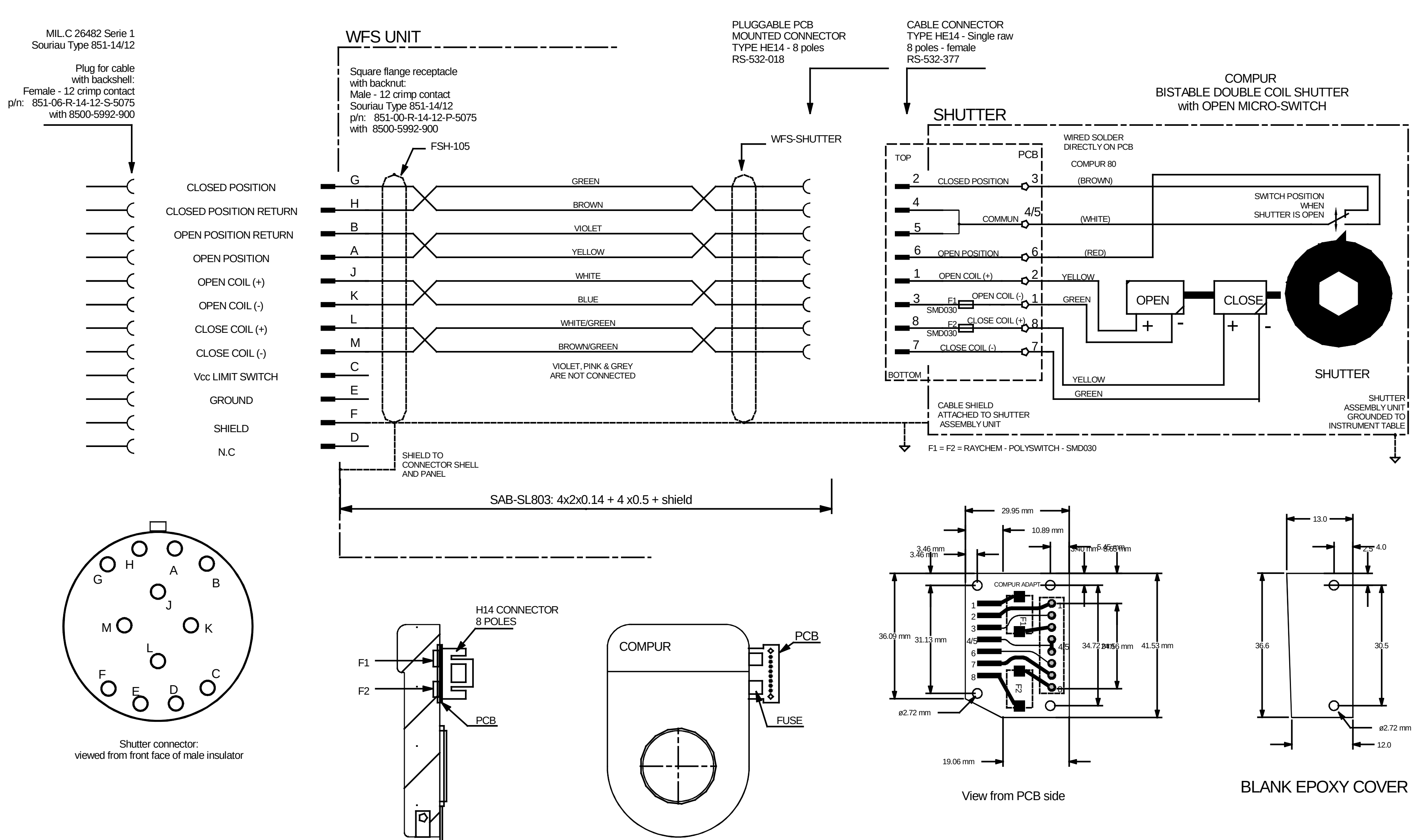
European Southern Observatory
MACAO-VLTI
Control Electronics

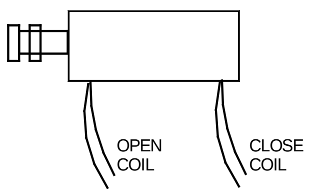
Title: WFS ENTRY SHUTTER CONTROLLER UNIT
FRONT PANEL LAYOUT

Page 1/1

Drawing Number: VLT-TRE-ESO-15600

File Name: RTC-SHUTTER-FP.skd



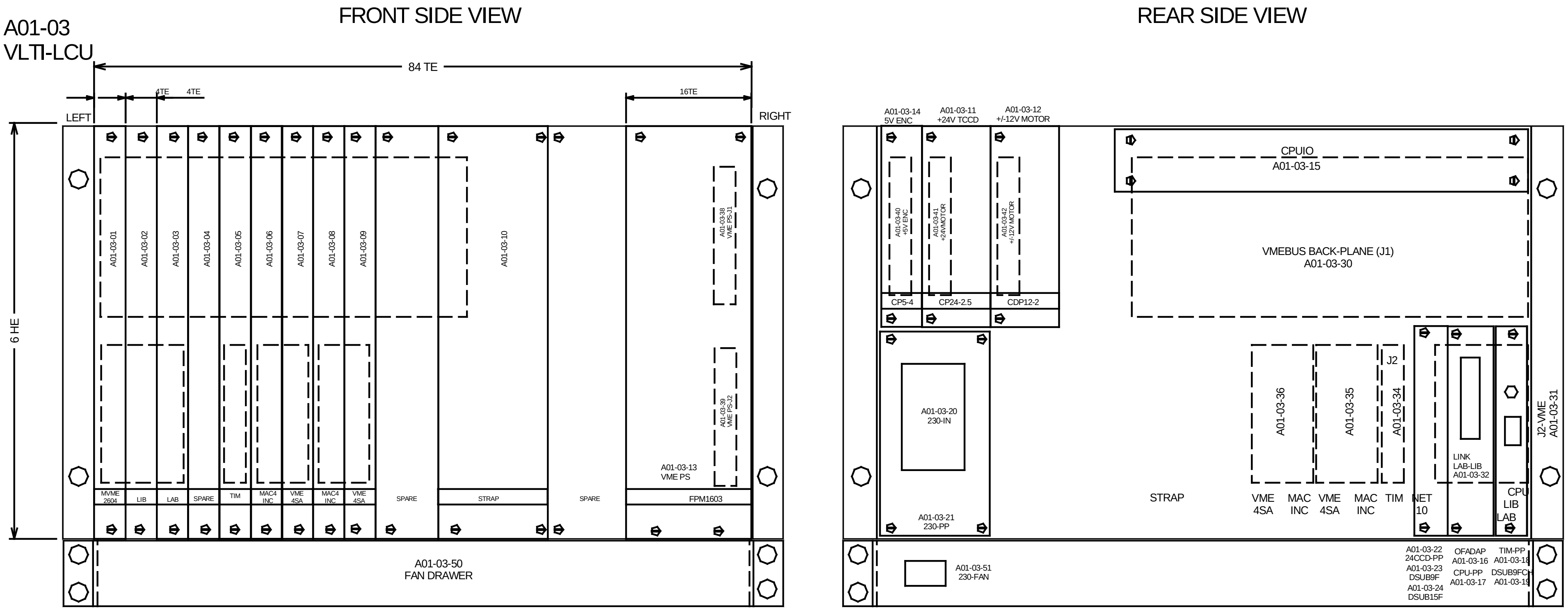


COMPUR SHUTTER
(RIGHT SIDE VIEW)

COMPUR SHUTTER
(FRONT VIEW)

					Date	Init.		
				Drawn	11/12/00	SM	European Southern Observatory MACO - VLTI Control Electronics	
				Chckd.				
				Appr.				
				Title: WFS Entry shutter Connector and cable assembly				Page 1/1
1	FDR	11/12/00	SM	Document Number: VLT-TRE-ESO-15600-2270			File Name: SHUT.skf	
Issue	Revisions	Date	Init					

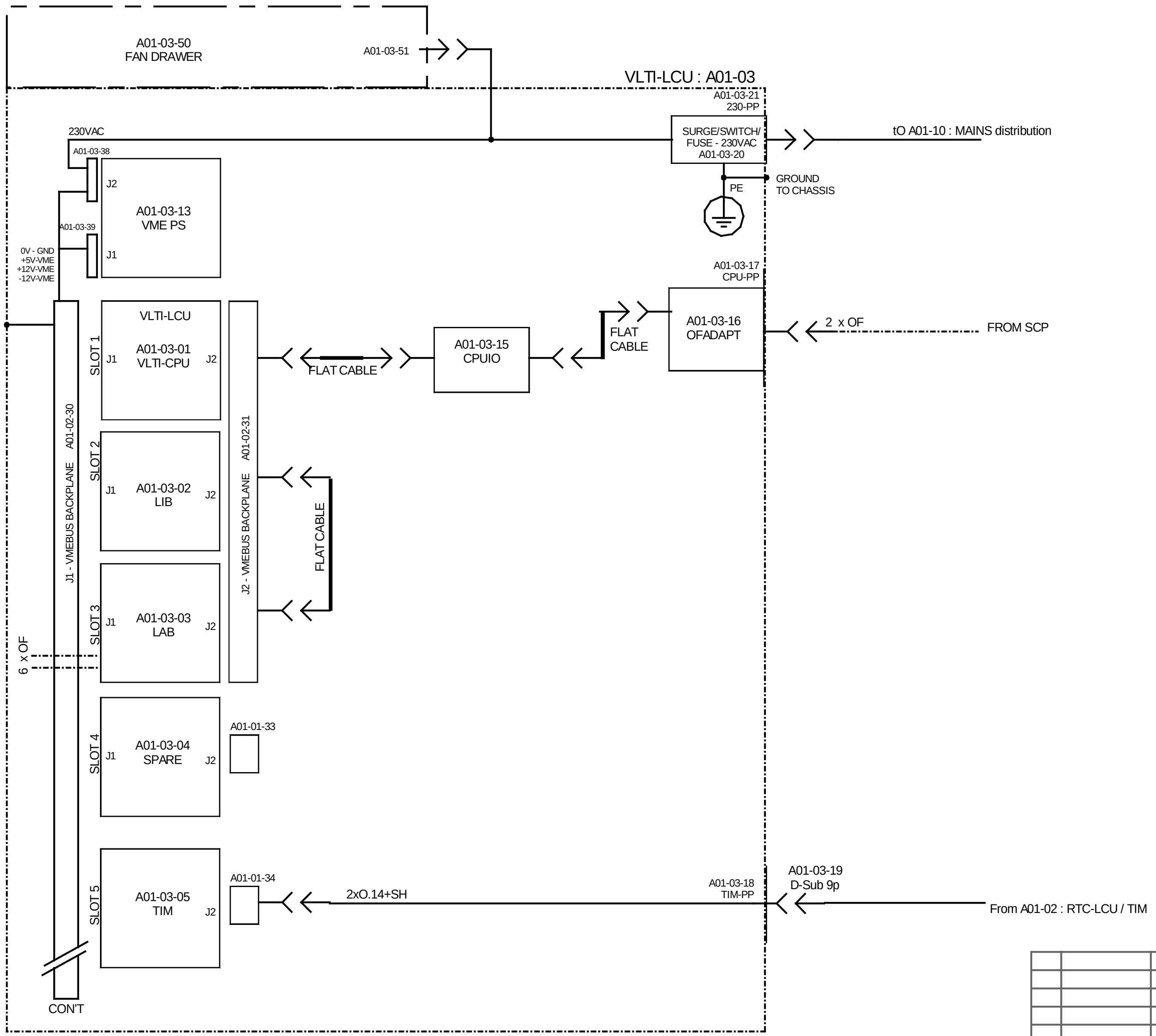
A01-03
VLTI-LCU



LEGENDE:

RACK: VERO - KM6-II - 420mm deepth
VMEBUS: 12 SLOTS - ACTIVE TERMINATION - AUTO DAISY CHAIN
CPUIO: TRANSITION MODULE MVME 712M
CPU:MOTOROLA - MVME2604 - with CONTROL LAN CONNECTION
LAB: ESO - LINK ADAPTOR BOARD
LIB: ESO - LINK INTERFACE BOARD
TIM: ESO - TIME INTERFACE VME MODULE
MAC4/INC: MACCON - MOTION CONTROLLER
VME4SA: ESO - DC SERVO-AMPLIFIER TYPE 1 (2Amps)
24V TCCD: KNIEL CP24-2.5
+/-12V MOTOR: KNIEL CDP12.2
5V ENCODER: KNIEL CP5-4
VME PS: KNIEL FPM 300W FOR VME BUS SUPPLY
CPU-PP: PATCH PANEL FOR LAN F.O
TIM-PP: PATCH PANEL FOR TRS F.O + RS422
STRAP: ESO - STRAP

					Date	Init.	
				Drawn	28/06/00	SM	European Southern Observatory MACAO-VLTI Control Electronics
				Chckd.			
				Appr.			
				Title: A01-03 : VLTI-LCU RACK LAYOUT			Page 1/1
3	FDR	30/10/00	SM				
2	AFC	30/10/00	SM				
1	PDR	28/09/00	SM	Document Number: VLT-SCH-ESO-15600-2270			File Name: VLTI-LCU_r3.skd
Issue	Revisions	Date	Init				



Issue	Revisions	Date	Init
2	AFC	30/10/00	SM
1	PDR	28/06/00	SM

	Date	Init.
Drawn	28/06/00	SM
Chckd.		
Appr.		

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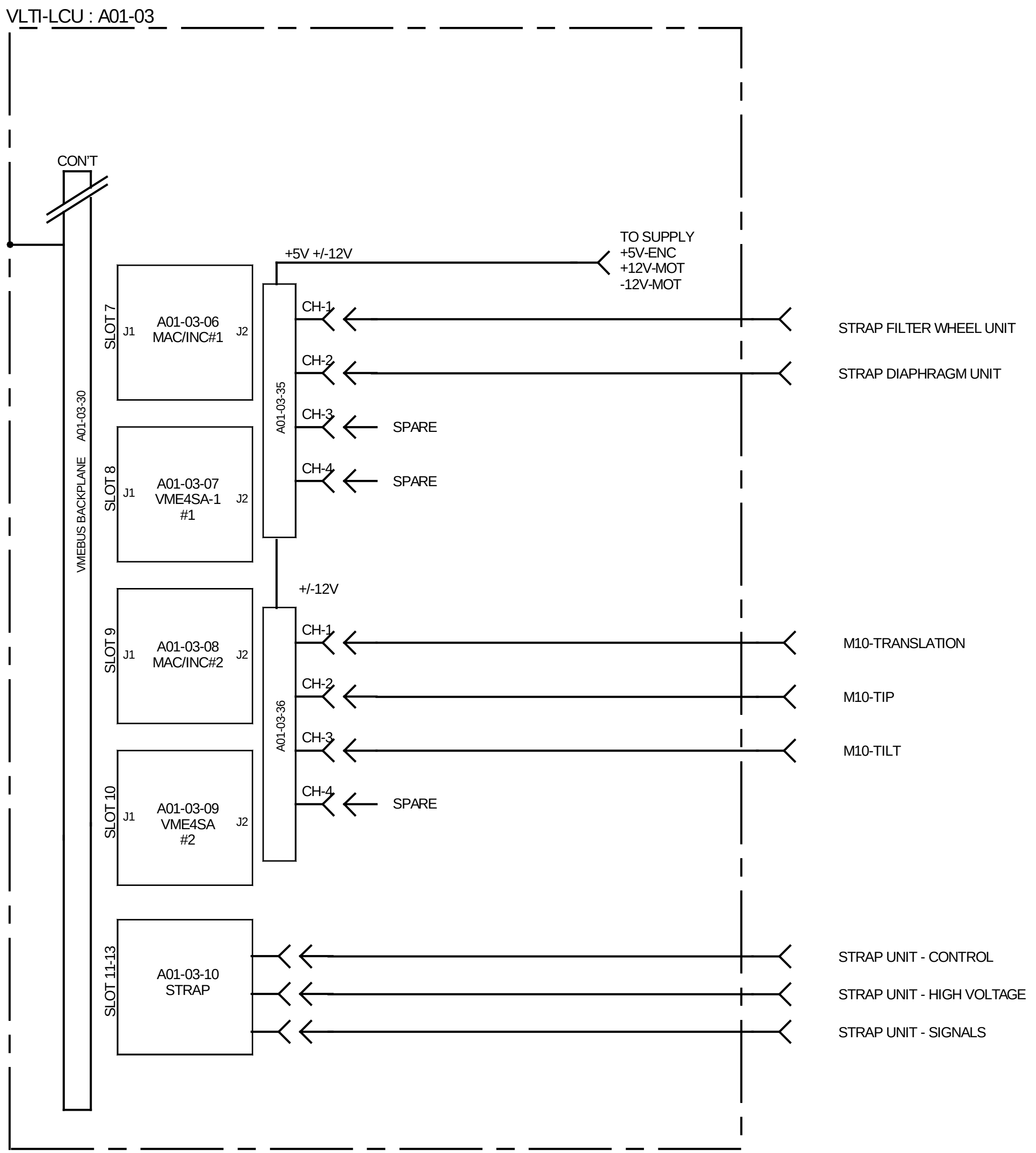
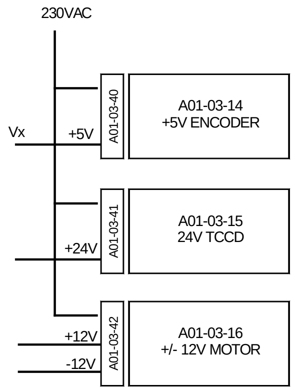
Title:
A01-03: VLTI-LCU
INTERNAL WIRING DIAGRAM (Part 1)

Page
1/1

Document Number:
VLT-SCH-ESO-15600-2270

File Name:
VLTI-LCU-01.skd





Issue	Revisions	Date	Init		Date	Init.
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				Chckd.		
				Appr.		
3	FDR	07/12/00	SM			
2	AFC	30/10/00	SM			
1	PDR	28/06/00	SM			

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Title: A01-03: VLTI-LCU
INTERNAL WIRING DIAGRAM (Part 2)

Page: 1/1

Document Number: VLT-SCH-ESO-15600-2270

File Name: VLTI-LCU-02.skd